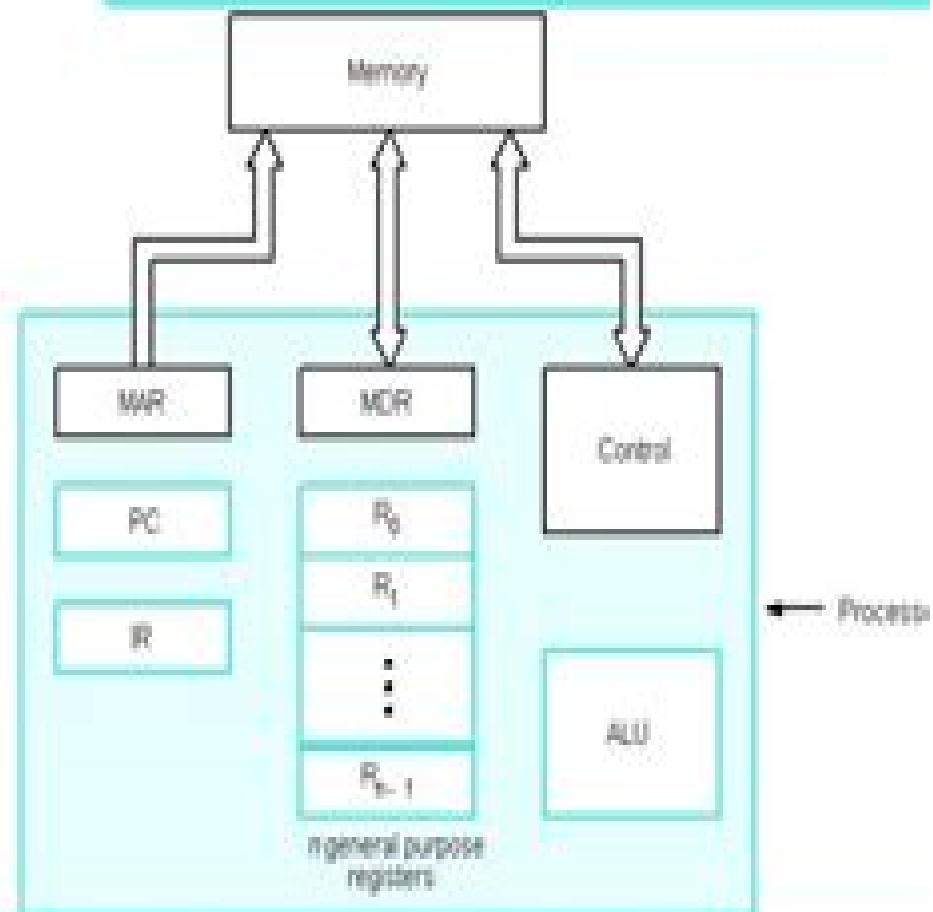


Digital Design and Computer Organization

Basic Structure of Computer: Basic operational Concepts part-2



Digital Design And Computer Organization

David Harris, Sarah Harris



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Digital Design and Computer Architecture David Harris, Sarah Harris, 2012-08-24 Digital Design and Computer Architecture Second Edition takes a unique and modern approach to digital design introducing the reader to the fundamentals of digital logic and then showing step by step how to build a MIPS microprocessor in both Verilog and VHDL. This new edition combines an engaging and humorous writing style with an updated and hands on approach to digital design. It presents new content on I/O systems in the context of general purpose processors found in a PC as well as microcontrollers found almost everywhere. Beginning with digital logic gates and progressing to the design of combinational and sequential circuits the book uses these fundamental building blocks as the basis for the design of an actual MIPS processor. It provides practical examples of how to interface with peripherals using RS232 SPI motor control interrupts wireless and analog to digital conversion. SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD based circuit design. There are also additional exercises and new examples of parallel and advanced architectures practical I/O applications embedded systems and heterogeneous computing plus a new appendix on C programming to strengthen the connection between programming and processor architecture. This new edition will appeal to professional computer engineers and to students taking a course that combines digital logic and computer architecture. Updated based on instructor feedback with more exercises and new examples of parallel and advanced architectures practical I/O applications embedded systems and heterogeneous computing. Presents digital system design examples in both VHDL and SystemVerilog updated for the second edition from Verilog shown side by side to compare and contrast their strengths. Includes a new chapter on C programming to provide necessary prerequisites and strengthen the connection between programming and processor architecture. Companion Web site includes links to Xilinx CAD tools for FPGA design lecture slides laboratory projects and solutions to exercises. Instructors can also register at textbooks.elsevier.com for access to Solutions to all exercises PDF Lab materials with solutions HDL for textbook examples and exercise solutions Lecture slides PPT Sample exams Sample course syllabus Figures from the text JPG PPT

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Digital Design and Computer Architecture Sarah Harris, David Harris, 2021-08-17 The newest addition to the Harris and Harris family of Digital Design and Computer Architecture books this RISC V Edition covers the fundamentals of digital logic design and reinforces logic concepts through the design of a RISC V microprocessor. Combining an engaging and humorous writing style with an updated and hands on approach to digital design this book takes the reader from the fundamentals of digital logic to the actual design of a processor. By the end of this book readers will be able to build their own RISC V microprocessor and will have a top to bottom understanding of how it works. Beginning with digital logic gates and progressing to the design of combinational and sequential circuits this book uses these fundamental building blocks as the basis for designing a RISC V processor. SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD based circuit design. The companion website includes a chapter on I/O systems with practical examples that show how to use SparkFun's RED V RedBoard to communicate with peripheral devices such as LCDs, Bluetooth radios and motors. This book will be a valuable resource for students taking a course that combines digital logic and computer architecture or students taking a two quarter sequence in digital logic and computer organization architecture. Covers the fundamentals of digital logic design and reinforces logic concepts through the design of a RISC V microprocessor. Gives students a full understanding of the RISC V instruction set architecture enabling them to build a RISC V processor and program the RISC V processor in hardware simulation, software simulation and in hardware. Includes both SystemVerilog and VHDL designs of fundamental building blocks as well as of single cycle, multicycle and pipelined versions of the RISC V architecture. Features a companion website with a bonus chapter on I/O systems with practical examples that show how to use SparkFun's RED V RedBoard to communicate with peripheral devices such as LCDs.

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Logic Design and Computer Organization Atul P. Godse, Dr. Deepali A. Godse, 2021-01-01 This book presents the basic concepts used in designing and analyzing digital circuits and introduces digital computer organization and design principles The first part of the book teaches you the number systems logic gates logic families Boolean algebra simplification of logic functions analysis and design of combinational circuits using SSI and MSI circuits It also explains latches and flip flops Types of counters synchronous and asynchronous counter design and applications and shift registers and its applications The second part of the book teaches you functional units of computer Von Neumann and Harvard architectures processor organization control unit hardwired control unit and microprogrammed control unit processor instructions instruction cycle instruction formats instruction pipelining RISC and CISC architectures interrupts interrupt handling multiprocessor systems multicore processors memory and I O organizations

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Digital Design (Verilog) Peter J. Ashenden, 2007-10-24 Digital Design An Embedded Systems Approach Using Verilog provides a foundation in digital design for students in computer engineering electrical engineering and computer science courses It takes an up to date and modern approach of presenting digital logic design as an activity in a larger systems design context Rather than focus on aspects of digital design that have little relevance in a realistic design context this book concentrates on modern and evolving knowledge and design skills Hardware description language HDL based design and verification is emphasized Verilog examples are used extensively throughout By treating digital logic as part of embedded systems design this book provides an understanding of the hardware needed in the analysis and design of systems comprising both hardware and software components Includes a Web site with links to vendor tools labs and tutorials Presents digital logic design as an activity in a larger systems design context Features extensive use of Verilog examples to demonstrate HDL hardware description language usage at the abstract behavioural level and register transfer level as well as for low level verification and verification environments Includes worked examples throughout to enhance the reader s understanding and retention of the material Companion Web site includes links to tools for FPGA design from Synplicity Mentor Graphics and Xilinx Verilog source code for all the examples in the book lecture slides laboratory projects and solutions to exercises

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In a global defined by information and interconnectivity, the enchanting power of words has acquired unparalleled significance. Their capability to kindle emotions, provoke contemplation, and ignite transformative change is really awe-inspiring. Enter the realm of "**Digital Design And Computer Organization**," a mesmerizing literary masterpiece penned with a distinguished author, guiding readers on a profound journey to unravel the secrets and potential hidden within every word. In this critique, we shall delve in to the book is central themes, examine its distinctive writing style, and assess its profound impact on the souls of its readers.

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