

Digital Logic and **Microprocessor Design** with VHDL



Enoch O. Hwang

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Digital Logic And Microprocessor Design With Vhdl

M. Rafiquzzaman



Digital Logic And Microprocessor Design With Vhdl:

Digital Logic and Microprocessor Design with VHDL Enoch O. Hwang, 2006 This book will teach students how to design digital logic circuits specifically combinational and sequential circuits Students will learn how to put these two types of circuits together to form dedicated and general purpose microprocessors This book is unique in that it combines the use of logic principles and the building of individual components to create data paths and control units and finally the building of real dedicated custom microprocessors and general purpose microprocessors After understanding the material in the book students will be able to design simple microprocessors and implement them in real hardware

Digital Logic And Microprocessor Design With Vhdl W/cd Hwang, **Advanced FDTD Methods** Wenhua Yu, 2011 Advanced FDTD Methods Parallelization Acceleration and Engineering Applications Contents Preface Chapter 1 Computational Electromagnetic Methods 1 1 FDTD METHOD 1 1 1 FDTD Update Equations 1 1 2 Stability Analysis 1 1 3 Boundary Conditions 1 2 METHOD OF MOMENTS 1 3 FINITE ELEMENT METHOD 1 3 1 Scalar Formulation 1 3 2 Vector Formulation 1 4 FINITE INTEGRATION TECHNIQUE References Chapter 2 FDTD Optimization and Acceleration 2 1 INTRODUCTION TO CPU ARCHITECTURE 2 2 SSE INSTRUCTION SET 2 3 CACHE OPTIMIZATION 2 4 TASK PARALLELIZATION AND BUNDLING 2 5 PREFETCH 2 6 READING OR WRITING COMBINATION 2 7 MATERIAL LOOP UP TABLE 2 8 NUMA OPTIMIZATION 2 9 IMPLEMENTATION OF VALU FDTD METHOD References Chapter 3 Parallel FDTD Method and Systems 3 1 PARALLEL FDTD METHOD 3 2 OPENMP FOR MULTICORE PROCESSORS 3 3 MPI TECHNIQUE 3 4 NETWORK CARD SWITCH AND CABLE References Chapter 4 Electromagnetic Simulation Techniques 4 1 MESH GENERATION TECHNIQUES 4 2 BASIC SIMULATION PROCEDURE 4 3 DIPOLE ANTENNA 4 4 VIVALDI ANTENNA SIMULATION 4 5 BANDED MICROWAVE CONNECTOR 4 6 PARALLEL LINES 4 7 TWO PORT ANTENNA 4 8 SLOT COUPLING 4 9 MICROWAVE FILTER 4 10 OPTIMIZATION AND PARAMETER SCAN 4 11 PERIODIC STRUCTURE SIMULATION 4 12 GROUND PENETRATING RADAR MODEL 4 13 MICROWAVE CONNECTOR References Chapter 5 EM Simulation Software Benchmarks 5 1 BASIC STEPS IN EM SIMULATION 5 1 1 HFSS 5 1 2 CST 5 1 3 FEKO 5 1 4 GEMS 5 2 HARDWARE PLATFORMS 5 3 PATCH ANTENNA 5 4 VIVALDI ANTENNA 5 5 SCATTERING OF DIELECTRIC SPHERE 5 6 CELL PHONE ANTENNA 5 7 ELECTROMAGNETIC BANDGAP STRUCTURE 5 8 STANDARD SAR TEST 5 9 WAVEGUIDE FILTER References Chapter 6 Large Multiscale Problem Solving 6 1 RADIO FREQUENCY PROTECTION

Fundamentals of Digital Logic and Microcomputer Design M. Rafiquzzaman, 2005-07-08 Fundamentals of Digital Logic and Microcomputer Design has long been hailed for its clear and simple presentation of the principles and basic tools required to design typical digital systems such as microcomputers In this Fifth Edition the author focuses on computer design at three levels the device level the logic level and the system level Basic topics are covered such as number systems and Boolean algebra combinational and sequential logic design as well as more advanced subjects such as assembly language programming and microprocessor

based system design Numerous examples are provided throughout the text Coverage includes Digital circuits at the gate and flip flop levels Analysis and design of combinational and sequential circuits Microcomputer organization architecture and programming concepts Design of computer instruction sets CPU memory and I O System design features associated with popular microprocessors from Intel and Motorola Future plans in microprocessor development An instructor's manual available upon request Additionally the accompanying CD ROM contains step by step procedures for installing and using Altera Quartus II software MASM 6 11 8086 and 68asm sim 68000 provides valuable simulation results via screen shots Fundamentals of Digital Logic and Microcomputer Design is an essential reference that will provide you with the fundamental tools you need to design typical digital systems

Digital Logic and Microprocessor Design with Interfacing Enoch O. Hwang, 2019

Embedded Microprocessor System Design using FPGAs Uwe Meyer-Baese, 2025-05-29 This textbook for courses in Embedded Systems introduces students to necessary concepts through a hands on approach It gives a great introduction to FPGA based microprocessor system design using state of the art boards tools and microprocessors from Altera Intel and Xilinx HDL based designs soft core parameterized cores Nios II and MicroBlaze and ARM Cortex A9 design are discussed compared and explored using many hand on designs projects Custom IP for HDMI coder Floating point operations and FFT bit swap are developed implemented tested and speed up is measured New additions in the second edition include bottom up and top down FPGA based Linux OS system designs for Altera Intel and Xilinx boards and application development running on the OS using modern popular programming languages Python Java and JavaScript HTML CSSs Downloadable files include all design examples such as basic processor synthesizable code for Xilinx and Altera tools for PicoBlaze MicroBlaze Nios II and ARMv7 architectures in VHDL and Verilog code as well as the custom IP projects For the three new OS enabled programming languages a substantial number of examples ranging from basic math and networking to image processing and video animations are provided Each Chapter has a substantial number of short quiz questions exercises and challenging projects

Uncertainty and Imprecision in Decision Making and Decision Support: Cross-Fertilization, New Models and Applications Krassimir T. Atanassov, Janusz Kacprzyk, Andrzej Kałuszko, Maciej Krawczak, Jan Owsinski, Sotir Sotirov, Evdokia Sotirova, Eulalia Szmidt, Sławomir Zadrozny, 2017-10-06 This book presents selected papers from two important conferences held on October 12 14 2016 in Warsaw Poland the Fourteenth National Conference of Operational and Systems Research BOS 2016 one of the premiere conferences in the field of operational and systems research not only in Poland but also at the European level and the Fifteenth International Workshop on Intuitionistic Fuzzy Sets and General Nets IWIFSGN 2016 one of the foremost conferences on fuzzy logic notably addressing extensions of the traditional fuzzy sets as well as the Generalized Nets GNs a powerful extension of the traditional Petri net paradigm The scope of the BOS conferences includes all types of problems related to systems modeling systems analysis broadly perceived operational research optimization decision making and decision support to name but a few In all these areas virtually all

models used have to take into account not only uncertainty in its traditional sense but also imprecision of information That is in addition to traditional probabilistic and statistical tools and techniques the use of methods based on fuzzy sets can also be sensible Even more so employing certain extensions of the classic concept of a fuzzy set can be very useful Applying intuitionistic fuzzy sets which are at the core of the IWIFSGN conferences is a good example Both conferences BOS 2016 and IWIFSGN 2016 offered ideal venues for the exchange of ideas cross fertilization and mutual inspiration *Digital Logic*

Circuits using VHDL Atul P. Godse, Dr. Deepali A. Godse, 2021-01-01 The book is written for an undergraduate course on digital electronics The book provides basic concepts procedures and several relevant examples to help the readers to understand the analysis and design of various digital circuits It also introduces hardware description language VHDL The book teaches you the logic gates logic families Boolean algebra simplification of logic functions analysis and design of combinational circuits using SSI and MSI circuits and analysis and design of the sequential circuits This book provides in depth information about multiplexers de multiplexers decoders encoders circuits for arithmetic operations various types of flip flops counters and registers It also covers asynchronous sequential circuits memories and programmable logic devices

FUNDAMENTALS OF DIGITAL LOGIC AND MICROCOMPUTER DESIGN, 5TH ED (With CD) M. Rafiquzzaman (with Cd), 2009-09-01 Market_Desc Undergraduate courses on digital logic design computer architecture and microprocessors Graduate students and practicing microprocessor system designers in industry Special Features While most texts either focus on computer design or digital logic and digital systems this book includes both areas making it a unique addition to existing literature The author has an extensive background in computers and has published numerous books on the subject He is undoubtedly one of the leading authorities in this field This book covers simple topics such as number system and Boolean algebra to advanced topics such as assembly language programming and microprocessor based system design The accompanying CD contains a step by step procedure for installing and using Altera Quartus II software for synthesizing Verilog and VHDL descriptions Screen shots of the waveforms and tabular forms illustrating the simulation results are also provided in the CD The CD also contains a step by step procedure for installing and using MASM 6 11 8086 and 68asmsim 68000 Screen shots verifying correct operations of several assembly language programs via simulation using test data are also provided in the CD About The Book This book covers all basic concepts of computer engineering and science from digital logic circuits to the design of a complete microcomputer system in a methodical and basic manner Its intention is to present a clear understanding of the principles and basic tools required to design typical digital systems such as microcomputers The book covers the latest version of Altera software called Quartus II It provides a simplified introduction to VHDL along with a step by step procedure with tutorials on a CD It is ideal for an introductory course in VHDL containing digital logic and microprocessors along with both VHDL and Verilog The material in the text is divided into three sections Fundamentals of digital logic circuits and design Microprocessor microcomputer design Overview of 16 32 and 64 bit microprocessors

manufactured by Intel and Motorola **Embedded Systems** Kiyofumi Tanaka, 2012-03-02 Nowadays embedded systems the computer systems that are embedded in various kinds of devices and play an important role of specific control functions have permitted various aspects of industry Therefore we can hardly discuss our life and society from now onwards without referring to embedded systems For wide ranging embedded systems to continue their growth a number of high quality fundamental and applied researches are indispensable This book contains 19 excellent chapters and addresses a wide spectrum of research topics on embedded systems including basic researches theoretical studies and practical work Embedded systems can be made only after fusing miscellaneous technologies together Various technologies condensed in this book will be helpful to researchers and engineers around the world

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