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Exercise Problems

3.1 Consider a MOS system with the following parameters:

a. Determine the threshold voltage V_m under zero bias at room temperature (T = 300 K). Note that ___ 3.97_a and __ 11.7_a.

SOLUTION:

First, calculate the Fermi potentials for the p-type substrate and for the n-type polysilicon gate:

(substrate)
$$\frac{kT_{\ln q}}{q} \frac{N_{a}}{N_{a}^{0.026}} = \frac{1.45 \times 10^{10}}{2.8 \times 10^{0.49}} = \frac{1.45 \times 10^{10}}{1.45 \times 10^{10}} = \frac{1.45 \times 1$$

The depletion region charge density at $V_{vir} = 0$ is found as follows:

$$Q_{ss}\sqrt{2 \ q \ N_{s-Sc} \ 2_{p} (substrate)}$$
 | $\sqrt{21.610^{cr} \ (2.810^{cr}) 11.7 \ 8.85 \ 10^{cr} \ 2 \ 0.49}$ | 9.53 10⁷ C/cm²

The oxide-interface charge is:

The gate oxide capacitance per unit area is calculated using the dielectric constant of silicon dioxide and the oxide thickness t_{car}

$$C_{ce} = \frac{3.978.8510^{14} \text{ F/cm}}{I_{ce}} = \frac{3.978.8510^{14} \text{ F/cm}^2}{1.610^7 \text{ cm}} = 2.210^6 \text{ F/cm}^2$$

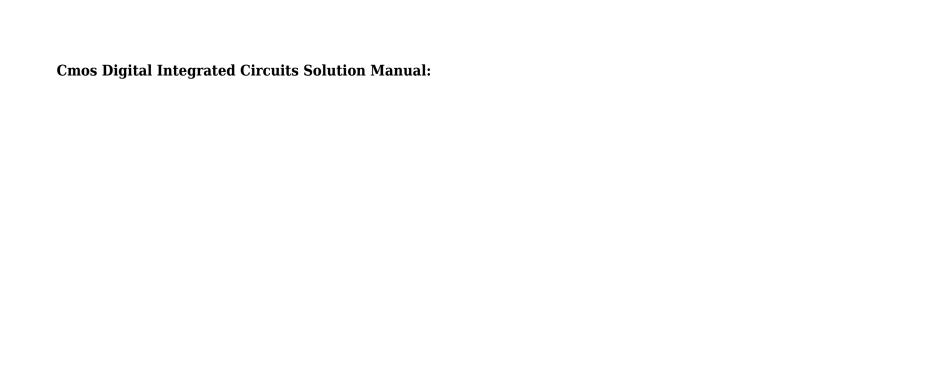
Now, we can combine all components and calculate the threshold voltage.

$$V_{r0-GC} = 2 \frac{Q_{sv}}{r} \frac{Q_{sv}}{C_{ss}} \frac{Q_{sv}}{C_{ss}} \frac{Q_{sv}}{C_{ss}}$$
1.04 (0.98) (0.53) (0.03) 0.44V

 Determine the type (p-type or n-type) and amount of channel implant (N_i/cm²) required to change the threshold voltage to 0.6V

Cmos Digital Integrated Circuits Solution Manual

Michal Rosen-Zvi



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