

Total No. of Questions : 8]

SEAT No. :

PA-1237

[Total No. of Pages : 2

[5925]-259

S.E. (Computer Engineering)

DIGITAL ELECTRONICS AND LOGIC DESIGN

(2019 Pattern) (Semester - III) (210245)

Time : 2½ Hours]

[Max. Marks : 70

Instructions to the candidates:

- 1) Attempt Q1 or Q2, Q3 or Q4, Q5 or Q6, Q7 or Q8.
- 2) Neat diagrams must be drawn wherever necessary.
- 3) Assume suitable data, if necessary.

Q1) a) What are sequential circuits? Explain excitation table of JK flipflop. [6]

b) Convert Following Flipflops: [6]

i) SR to JK

ii) JK to D

c) What is MOD counter? Design MOD - 24 counter using 7490. [6]

OR

Q2) a) What are sequential circuits? Explain SR flipflop using a suitable example. [6]

b) Convert Following Flipflops: [6]

i) JK to T

ii) SR to D

c) Design sequence detector using MS JK flipflop for sequence 1101. [6]

Q3) a) Draw ASM chart for 2-bit UP counter using multiplexer controller method. [6]

b) Draw a block diagram of the PLA device and explain. [6]

c) Implement following Boolean function using PAL. [5]

$$F1 = \sum m(0, 2, 3, 4, 5, 6, 7, 8, 10, 11, 15)$$

$$F2 = \sum m(1, 2, 8, 12, 13)$$

OR

P.T.O.

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including CMOS logic Programmable Logic Devices and microprocessor microcontroller interfacing Courses teaching introductory digital electronics will fill in the missing areas of information for students but neither the instructors nor students have resources to explain modern technology and interfaces One assumption made by all the standard texts is that experimenting with digital electronics cannot be done easily in the proposed book digital guru Myke Predko will show how readers can set up their own apparatus for experimenting with digital electronics for less than 10

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entry level students research scholars professionals and even the general readers alike with a new vision to have a quick yet in depth glimpse of the progress of wireless technology since its inception till today It is primarily written for junior undergraduate students in the fields of electrical engineering electronics and communication engineering wireless communication telecommunication and computer engineering Research scholars and professionals can also refresh their idea of the developments of wireless technology during its formative stage and can get a flavour of the route to modern developments that have taken place henceforth SSC Junior Engineer Electrical Recruitment Exam Guide 3rd Edition

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volume set CCIS 243 and CCIS 244 constitutes the refereed proceedings of the Second International Conference on Information Computing and Applications ICICA 2010 held in Qinhuangdao China in October 2011 The 191 papers presented in both volumes were carefully reviewed and selected from numerous submissions They are organized in topical sections on computational statistics social networking and computing evolutionary computing and applications information education and application internet and web computing scientific and engineering computing system simulation computing bio inspired and DNA computing internet and Web computing multimedia networking and computing parallel and distributed computing

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