#### Lab No. 01

#### INTRODUCTION TO DIGITAL LOGIC DESIGN LAB EQUIPMENT

#### Objective:

To Study the operation of different parts of Digital

Analog Training System (ETS-7000)

#### Tools:

#### 1- Digital Analog Trainer (ETS-7000):

The digital analog trainer is designed for beginners to enhance their concepts about digital and analog theory.

It has a solderless breadboard with other parts like switches, LEDs, Seven Segment Display and other slots for wires.



It is used for the prototyping and hardware simulation of a circuit. It is used to test and check a circuit before soldering the circuit components on a PCB or a Vero board. It makes the testing and analysis of a circuit, a reversible and easy to troubleshoot process unlike the analysis of a circuit on a Vero board.

#### 3- Jumper Wire:

This wire is used with a breadboard to transmit electric signals. It is used as a conductor with high conductivity and low resistivity.

#### 4- Logic Indicator:

This indicator is mounted on the trainer and is used to monitor the logic state of the circuit.

#### 5- Logic Switch:

It is a diode arrangement present on the ETS-7000 trainer and is capable of directing an input signal.

#### 6- DC Power Supply:

It is an electronic device used in order to supply DC energy to an electrical load.



Figure 1: ETS-7000 (Digital Analog Trainer)

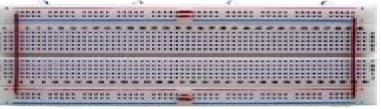




Figure 2: Aumper Wires



# **Digital Logic Device Lab Manual**

**Robert K. Dueck, Leo Chartrand** 

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problems provide a rich set of assessment tools to measure learner performance on each outcome This book can be used for either a sequence of two courses consisting of an introduction to logic circuits Chapters 1 7 followed by logic design Chapters 8 13 or a single accelerated course that uses the early chapters as reference material **Digital Design from Zero to One** Jerry D. Daniels, 1996-04-19 Takes a fresh look at basic digital design From definition to example to graphic illustration to simulation result the book progresses through the main themes of digital design Technically up to date this book covers all the latest topics Field programmable gate arrays PALs and ROMs The latest memory chips for SRAM and DRAM are shown Software for creating the excitation equations of FSM are covered as well as LogicWorks and Beige Bag PC Digital Electronics and Laboratory Computer Experiments Charles Wilkins, 2012-12-06 Science and more undergraduates have come to accept the use of computers as commonplace The daily use of portable sophisticated electronic calculators some of them rivaling general purpose minicomputers in their capa bili ties has hastened this development Over the past several years computer assisted experimentation has assumed an important role in the experimental laboratory Mini and microcomputer systems have become an important part of the physical scientist's array of analytical instruments Prompted by our beliefthat this was an inevitable development we began several years aga to develop the curricular materials presented in this manual At the outset several objectives seemed important to uso First insofar as possible the experiments included should be thoroughly tested and error free Second they should be compatible with a variety of laboratory computer data acquisition and control systems Third little or no previous background in either electronics or programming should be necessary Of course such background would be advantageous To satisfy these objectives we decided to adopt a widespread high level computer language BASIC suitably modified for the purpose Furthermore we have purposely avoided specifying any particular system or equipment Rather the functional characteristics of both hardware and software required are stipulated The experiments have been developed using Varian 620 and Hewlett Packard 2100 series computers but we believe they are readily transferable to other commonly available computer systems with a minimum of difficulty

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# **Table of Contents Digital Logic Device Lab Manual**

- 1. Understanding the eBook Digital Logic Device Lab Manual
  - The Rise of Digital Reading Digital Logic Device Lab Manual
  - Advantages of eBooks Over Traditional Books
- 2. Identifying Digital Logic Device Lab Manual
  - Exploring Different Genres
  - Considering Fiction vs. Non-Fiction
  - Determining Your Reading Goals
- 3. Choosing the Right eBook Platform
  - Popular eBook Platforms
  - Features to Look for in an Digital Logic Device Lab Manual
  - User-Friendly Interface
- 4. Exploring eBook Recommendations from Digital Logic Device Lab Manual
  - Personalized Recommendations
  - Digital Logic Device Lab Manual User Reviews and Ratings
  - o Digital Logic Device Lab Manual and Bestseller Lists

- 5. Accessing Digital Logic Device Lab Manual Free and Paid eBooks
  - o Digital Logic Device Lab Manual Public Domain eBooks
  - o Digital Logic Device Lab Manual eBook Subscription Services
  - Digital Logic Device Lab Manual Budget-Friendly Options
- 6. Navigating Digital Logic Device Lab Manual eBook Formats
  - ∘ ePub, PDF, MOBI, and More
  - Digital Logic Device Lab Manual Compatibility with Devices
  - Digital Logic Device Lab Manual Enhanced eBook Features
- 7. Enhancing Your Reading Experience
  - Adjustable Fonts and Text Sizes of Digital Logic Device Lab Manual
  - Highlighting and Note-Taking Digital Logic Device Lab Manual
  - Interactive Elements Digital Logic Device Lab Manual
- 8. Staying Engaged with Digital Logic Device Lab Manual
  - o Joining Online Reading Communities
  - Participating in Virtual Book Clubs
  - Following Authors and Publishers Digital Logic Device Lab Manual
- 9. Balancing eBooks and Physical Books Digital Logic Device Lab Manual
  - Benefits of a Digital Library
  - Creating a Diverse Reading Collection Digital Logic Device Lab Manual
- 10. Overcoming Reading Challenges
  - Dealing with Digital Eye Strain
  - Minimizing Distractions
  - Managing Screen Time
- 11. Cultivating a Reading Routine Digital Logic Device Lab Manual
  - Setting Reading Goals Digital Logic Device Lab Manual
  - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Digital Logic Device Lab Manual
  - Fact-Checking eBook Content of Digital Logic Device Lab Manual
  - Distinguishing Credible Sources
- 13. Promoting Lifelong Learning

- Utilizing eBooks for Skill Development
- Exploring Educational eBooks
- 14. Embracing eBook Trends
  - Integration of Multimedia Elements
  - Interactive and Gamified eBooks

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