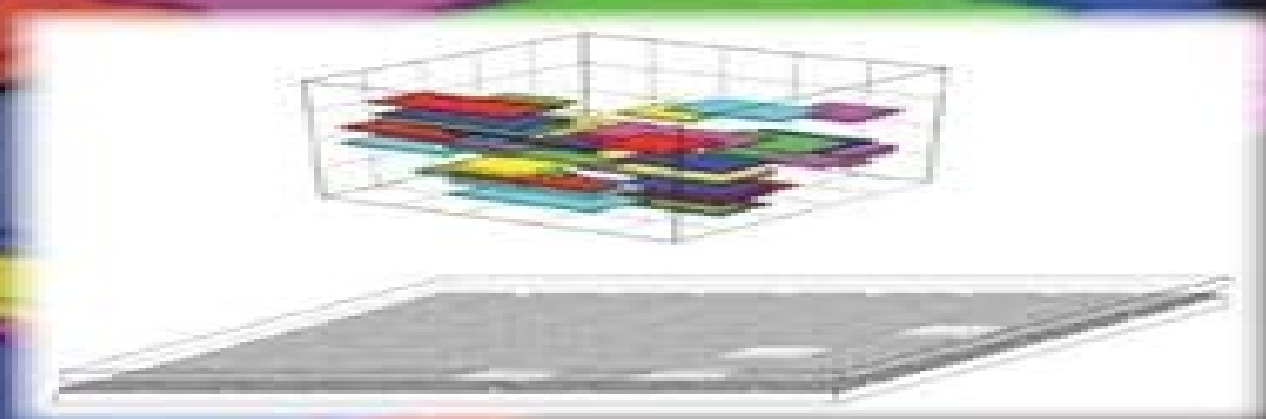


DIGITAL MICROFLUIDIC BIOCHIPS

**SYNTHESIS, TESTING,
AND RECONFIGURATION
TECHNIQUES**



**Krishnendu Chakrabarty
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Digital Microfluidic Biochips Synthesis Testing And Reconfiguration Techniques

Krishnendu Chakrabarty, Tao Xu



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for digital microfluidic biochips that unifies different design problems With the increase in system complexity and integration levels biochip designers can utilize the design methods described in this book to evaluate different design alternatives and carry out design space exploration to obtain the best design point **Digital Microfluidic Biochips** Krishnendu

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Chakrabarty,Tsung-Yi Ho,2017-04-05 This book provides a comprehensive overview of flow based microfluidic VLSI The authors describe and solve in a comprehensive and holistic manner practical challenges such as control synthesis wash optimization design for testability and diagnosis of modern flow based microfluidic biochips They introduce practical solutions based on rigorous optimization and formal models The technical contributions presented in this book will not only shorten the product development cycle but also accelerate the adoption and further development of modern flow based microfluidic biochips by facilitating the full exploitation of design complexities that are possible with current fabrication techniques **Transactions on Computational Science XXXV** Marina L. Gavrilova,C. J. Kenneth Tan,Khalid

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book describes for researchers in the fields of compiler technology design and test and electronic design automation the new area of digital microfluidic biochips DMBs and thus offers a new application area for their methods The authors present a routing based model of operation execution along with several associated compilation approaches which progressively relax the assumption that operations execute inside fixed rectangular modules Since operations can experience transient faults

during the execution of a bioassay the authors show how to use both offline design time and online runtime recovery strategies The book also presents methods for the synthesis of fault tolerant application specific DMB architectures Presents the current models used for the research on compilation and synthesis techniques of DMBs in a tutorial fashion Includes a set of benchmarks which are presented in great detail and includes the source code of most of the techniques presented including solutions to the basic compilation and synthesis problems Discusses several new research problems in detail using numerous examples Advancing Computational Intelligence Techniques for Security Systems Design Uzzal Sharma,Parmanand Astya,Anupam Baliyan,Salah-ddine Krit,Vishal Jain,Mohammad Zubair Khan,2022-08-24 Security systems have become an integral part of the building and large complex setups and intervention of the computational intelligence CI paradigm plays an important role in security system architecture This book covers both theoretical contributions and practical applications in security system design by applying the Internet of Things IoT and CI It further explains the application of IoT in the design of modern security systems and how IoT blended with computational intel ligence can make any security system improved and realizable Key features Focuses on the computational intelligence techniques of security system design Covers applications and algorithms of discussed computational intelligence techniques Includes convergence based and enterprise integrated security systems with their applications Explains emerging laws policies and tools affecting the landscape of cyber security Discusses application of sensors toward the design of security systems This book will be useful for graduate students and researchers in electrical computer engineering security system design and engineering

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