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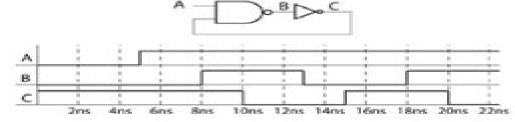
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#### Chapter 2 Solutions

- 2.1 (a) VHDL VHSIC Hardware Description Language VHSIC - Very High Speed Integrated Circuit
  - (b) VHDL has statements that execute concurrently since it must model real hardware in which the components are all in operation at the same time.
  - (c) A hardware description language allows a digital system to be designed and debugged at a higher level of abstraction than schematic capture with gates, flip-flops, and standard MSI building blocks. The details of the gates and flip-flops do not need to be handled during early phases of design. Designs are more portable when low-level library-specific details are not included in the model. HDLs allow the creation of such portable high-level behavioral models.
- 2.2 (a) Legal: A\_123, and 1: Hiegal: 123A (Starts with number), \_A123 (starts with underscore), A123\_ (ends with underscore), e1 e2 £adjacent underscores), and (reserved word).
  - (b) They all equivalent. VHDL is not case sensitive.

2.3 (a)

Obt



2.4 entity Comb is port(A. B. C. D : in bit) 2 : out bit): emd Comby architecture Comb2\_1 of Comb is signal E. F. G. H. I : bit: beegin. and B and C) after 5 ns. 歌の神様と or pafter 5 max Committee (CE) norC) after 5 ns; World. nand A after 5 ns; I - not F after 2 na: I was now I after 5 max end Comb2 1: 2.5 (a) entity one\_bit\_sub is port(x, y, bin: in bit; diff, bout: out bit);

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R.D. Hunter, T. T. Johnson

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industry standard designs Many circuits shown with internal details at the transistor level as in real integrated circuits Actual technologies used in state of the art digital circuits presented in conjunction with fundamental concepts and principles Six chapters dedicated to VHDL based techniques with all VHDL based designs synthesized onto CPLD FPGA chips **FPGA Prototyping by VHDL Examples** Pong P. Chu,2011-09-20 This book uses a learn by doing approach to introduce the concepts and techniques of VHDL and FPGA to designers through a series of hands on experiments FPGA Prototyping by VHDL Examples provides a collection of clear easy to follow templates for quick code development a large number of practical examples to illustrate and reinforce the concepts and design techniques realistic projects that can be implemented and tested on a Xilinx prototyping board and a thorough exploration of the Xilinx PicoBlaze soft core microcontroller **Digital Principles and Design** Donald D. Givone,2003 Instructor's Solutions Manual to Accompany Fundamentals of Digital Logic with Vhdl Design Lord Brown,Stephen D. Brown,Zvonko G. Vranesic,1999-10-01 **Books in Print**,1994

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