

Solution Manual for Digital Systems Design Using VHDL 3rd Edition by Roth John
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Chapter 2 Solutions

- 2.1 (a) VHDL - VHSIC Hardware Description Language
VHSIC - Very High Speed Integrated Circuit
- (b) VHDL has statements that execute concurrently since it must model real hardware in which the components are all in operation at the same time.
- (c) A hardware description language allows a digital system to be designed and debugged at a higher level of abstraction than schematic capture with gates, flip-flops, and standard MSI building blocks. The details of the gates and flip-flops do not need to be handled during early phases of design. Designs are more portable when low-level library-specific details are not included in the model. HDLs allow the creation of such portable high-level behavioral models.

- 2.2 (a) Legal: A_123, and1; Illegal: 123A (Starts with number), _A123 (starts with underscore), A123_ (ends with underscore), c1 c2 (adjacent underscores), and (reserved word).

(b) They all equivalent. VHDL is not case sensitive.

- 2.3 (a)



- (b)



```
2.4 entity Comb is
    port(A, B, C, D : in bit;
          Z : out bit);
end Comb;
architecture Comb2_1 of Comb is
    signal E, F, G, H, I : bit;
begin
    H<=(A and B and C) after 5 ns;
    E<=H or D after 5 ns;
    G<=(B nor C) after 5 ns;
    F<=G nand A after 5 ns;
    I <= not F after 2 ns;
    Z<=E xor I after 5 ns;
end Comb2_1;
```

- 2.5 (a) entity one_bit_sub is
port(x, y, bin: in bit;
diff, bout: out bit);

Digital Electronics With Vhdl Solution Manual

R.D. Hunter,T. T. Johnson



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Introduction to VHDL, 1997 **Introduction to VHDL** R.D. Hunter, T. T. Johnson, 1996 Covers all aspects of the VHDL language *Digital System Design Using VHDL* Rishabh Anand, 2023 The book covers the complete syllabus of subject as suggested by most of the universities in India Generic VHDL code is taught and used through out the book so that different companies VHDL tools can be used if desired Moving from the unknown in a logical manner Subject matter in each chapter develops systematically from inceptions Large number of carefully selected worked examples in sufficient details No other reference is required Ideally suited for self study *Embedded SoPC Design with Nios II Processor and VHDL Examples* Pong P. Chu, 2011-08-29 The book is divided into four major parts Part I covers HDL constructs and synthesis of basic digital circuits Part II provides an overview of embedded software development with the emphasis on low level I O access and drivers Part III demonstrates the design and development of hardware and software for several complex I O peripherals including PS2 keyboard and mouse a graphic video controller an audio codec and an SD secure digital card Part IV provides three case studies of the integration of hardware accelerators including a custom GCD greatest common divisor circuit a Mandelbrot set fractal circuit and an audio synthesizer based on DDFS direct digital frequency synthesis methodology The book utilizes FPGA devices Nios II soft core processor and development platform from Altera Co which is one of the two main FPGA manufactures Altera has a generous university program that provides free software and discounted prototyping boards for educational institutions details at www.altera.com/university The two main educational prototyping boards are known as DE1 99 and DE2 69 All experiments can be implemented and tested with these boards A board combined with this book becomes a turn key solution for the SoPC design experiments and projects Most HDL and C codes in the book are device independent and can be adapted by other prototyping boards as long as a board has similar I O configuration *Digital Electronics and Design with VHDL* Volnei A. Pedroni, 2008-01-25 Digital Electronics and Design with VHDL offers a friendly presentation of the fundamental principles and practices of modern digital design Unlike any other book in this field transistor level implementations are also included which allow the readers to gain a solid understanding of a circuit's real potential and limitations and to develop a realistic perspective on the practical design of actual integrated circuits Coverage includes the largest selection available of digital circuits in all categories combinational sequential logical or arithmetic and detailed digital design techniques with a thorough discussion on state machine modeling for the analysis and design of complex sequential systems Key technologies used in modern circuits are also described including Bipolar MOS ROM RAM and CPLD FPGA chips as well as codes and techniques used in data storage and transmission Designs are illustrated by means of complete realistic applications using VHDL where the complete code comments and simulation results are included This text is ideal for courses in Digital Design Digital Logic Digital Electronics VLSI and VHDL and industry practitioners in digital electronics Comprehensive coverage of fundamental digital concepts and principles as well as complete realistic

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