

Digital System Designs and Practices

Using Verilog HDL
and FPGAs

Ming-Bo Lin

 WILEY

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Digital System Designs And Practices Using Verilog Hdl And Fpgas

Ming-Bo Lin



Digital System Designs And Practices Using Verilog Hdl And Fpgas:

Digital System Design with FPGA: Implementation Using Verilog and VHDL Cem Unsalan, Bora Tar, 2017-07-14 Master FPGA digital system design and implementation with Verilog and VHDL This practical guide explores the development and deployment of FPGA based digital systems using the two most popular hardware description languages Verilog and VHDL Written by a pair of digital circuit design experts the book offers a solid grounding in FPGA principles practices and applications and provides an overview of more complex topics Important concepts are demonstrated through real world examples ready to run code and inexpensive start to finish projects for both the Basys and Arty boards Digital System Design with FPGA Implementation Using Verilog and VHDL covers Field programmable gate array fundamentals Basys and Arty FPGA boards The Vivado design suite Verilog and VHDL Data types and operators Combinational circuits and circuit blocks Data storage elements and sequential circuits Soft core microcontroller and digital interfacing Advanced FPGA applications The future of FPGA

Digital System Designs and Practices Ming-Bo Lin, 2008-10-13 System on a chip SoC has become an essential technique to lower product costs and maximize power efficiency particularly as the mobility and size requirements of electronics continues to grow It has therefore become increasingly important for electrical engineers to develop a strong understanding of the key stages of hardware description language HDL design flow based on cell based libraries or field programmable gate array FPGA devices Honed and revised through years of classroom use Lin focuses on developing verifying and synthesizing designs of practical digital systems using the most widely used hardware description Language Verilog HDL Explains how to perform synthesis and verification to achieve optimized synthesis results and compiler times Offers complete coverage of Verilog syntax Illustrates the entire design and verification flow using an FPGA case study Presents real world design examples such as LED and LCD displays GPIO UART timers and CPUs Emphasizes design implementation tradeoff options with coverage of ASICs and FPGAs Provides an introduction to design for testability Gives readers deeper understanding by using problems and review questions in each chapter Comes with downloadable Verilog HDL source code for most examples in the text Includes presentation slides of all book figures for student reference Digital System Designs and Practices Using Verilog HDL and FPGAs is an ideal textbook for either fundamental or advanced digital design courses beyond the digital logic design level Design engineers who want to become more proficient users of Verilog HDL as well as design FPGAs with greater speed and accuracy will find this book indispensable

Digital Systems Design and Practice Ming-Bo Lin, 2015-07-27 With the advance of semiconductor and communication technologies the use of system on a chip SoC has become an essential technique to decrease product costs To design and implement an SoC based product it proves necessary to totally or partly rely on the hardware description language HDL synthesis flow and field programmable gate array FPGA devices or cell libraries As a consequence it has become an important attainment for electrical engineers to develop a good understanding of the key issues of HDL design flows based on FPGA devices or cell

libraries To achieve this this book addresses the need for teaching such a topic based on Verilog HDL and FPGAs This book Digital System Designs and Practices Using Verilog HDL and FPGAs aim to be used as a text for students and as a reference book for professionals or a self study book for readers For classroom use each chapter includes many worked examples and review questions for helping readers test their understanding of the contents In addition throughout the book an abundance of worked examples are provided for helping readers realize the basic features of Verilog HDL and grasp the essentials of digital system designs as well The contents of this book largely stem from the course FPGA System Designs and Practices offered at our campus over the past decade This course is an undergraduate elective and the first year graduate course This book is so structured that it can be used as a sequence of courses including Hardware Description Language FPGA System Designs and Practices Digital System Designs Advanced Digital System Designs and others HDL based design has become an essential technique for modern digital systems This book focuses on developing verifying and synthesizing designs of practical digital systems using the most widely used hardware description Language Verilog HDL and FPGAs The main features of this book are Explains how to perform synthesis and verification to achieve optimized synthesis results and compiler times Offers complete coverage of Verilog HDL syntax Illustrates the entire design and verification flow using an FPGA case study Presents many real world worked design examples Gives readers deeper understanding with review questions in each section and end of chapter problems Emphasizes design implementation tradeoff options with coverage of ASICs and FPGAs

Introduction to VLSI Systems Ming-Bo Lin, 2011-11-28 With the advance of semiconductors and ubiquitous computing the use of system on a chip SoC has become an essential technique to reduce product cost With this progress and continuous reduction of feature sizes and the development of very large scale integration VLSI circuits addressing the harder problems requires fundamental understanding

Digital VLSI Systems Design Seetharaman Ramachandran, 2007-06-14 This book deals with actual design applications rather than the technology of VLSI Systems This book is written basically for an advanced level course in Digital VLSI Systems Design using a Hardware Design Language HDL V ilog This book may be used for teaching undergraduates graduates and research scholars of Electrical Electronics Computer Science and Engineering Embedded Systems Measurements and Instrumentation Applied Electronics and interdisciplinary departments such as Biomedical Mechanical Engineering Information Technology Physics etc This book also serves as a reference design manual for practicing engineers and researchers Although this book is written for an vanced level course diligent freelance readers and consultants especially those who do not have a first level exposure of digital logic design may also start using this book after a short term course or self study on digital logic design In order to help these readers as well as regular students the book starts with a good review of digital systems design which lays a solid foundation to understand the rest of this book right up to involved Project Designs unfolded gradually Contents of the Book The book presents new source material and theory as well as synthesis of recent work with complete Project Designs using industry standard CAD

tools and FPGA boards enabling the serious readers to design VLSI Systems on their own **FPGA -Based Systems**

Design and Practice Ming-Bo Lin, 2018-07-30 With the advance of semiconductor and communication industry the use of system on chip SoC has become an essential technique to reduce product costs The development of a good understanding of the key stages of the hardware description language HDL design flow based on cell based libraries or field programmable gate array FPGA devices becomes essential This book addresses the needs for such a topic based on Verilog HDL and FPGAs The most important features of this book include HDL based design has become an essential technique for modern digital systems This book focuses on developing verifying and synthesizing designs of practical digital systems using the most widely used hardware description Language Verilog HDL and FPGAs The main features of this book include Explaining how to perform synthesis and verification to achieve optimized synthesis results and compiler times Illustrating the entire design and verification flow using an FPGA case study Emphasizing design implementation trade off options with coverage of ASICs and FPGAs Providing plentiful worked examples and review questions in each section for readers to test their understanding of the related topics Giving readers deeper understanding with plentiful review questions in each section and end of chapter problems Incorporating many case studies to help the reader grasp the essentials of practical digital systems to be designed using Verilog HDL and FPGAs Highlighting Verilog HDL syntax throughout the book to facilitate readers to refer the desired syntax as they need Printing all keywords in boldface throughout the book to emphasize the language structures and improve the readability of Verilog HDL modules This book is the ideal textbook for the following courses Digital System Design FPGA System Designs and Practices Advanced Digital Systems Design and the like In addition it can be used as a self studying or professional reference book in this field *A Tutorial on Fpga-Based System Design Using Verilog Hdl* Ming-Bo

Lin, 2018-08-17 The contents of this book are designed on the basis of the problem based learning PBL approach and follow the paradigm design entry in both schematic and HDL verification as well as implementation Based on this paradigm we develop an incremental learn by doing method to help the student to build a sound understanding in both the design principles and the implementations of digital systems based on FPGA devices Features of this book include Lab projects are exercised with schematic entry first and then Verilog HDL entry Both functional and timing verification are performed in each entry method to ensure the resulting design can work properly in FPGA devices The incremental learn by doing method is applied to gradually introduce new concepts and hardware resources and increase the depth of lab projects The paradigm design entry in both schematic and HDL verification as well as implementation is employed to familiarize the reader with the right concept and use of the HDL entry method Optional lab projects are provided for readers to make realistic tests on FPGA devices Extended lab projects to broaden the reader's background knowledge and capability This book can be used as the textbook for the following courses Digital Logic Design Practice Introduction to FPGA Based System Design Introduction to Digital System Practice and Introduction to Verilog HDL **FPGA Systems Design and Practice** Ming-Bo Lin, 2016-03-04

With the advance of semiconductor and communication industry the use of system on chip SoC has become an essential technique to reduce product costs. The development of a good understanding of the key stages of the hardware description language HDL design flow based on cell based libraries or field programmable gate array FPGA devices becomes essential. This book addresses the needs for such a topic based on Verilog HDL and FPGAs. The most important features of this book include:

- HDL based design has become an essential technique for modern digital systems. This book focuses on developing, verifying and synthesizing designs of practical digital systems using the most widely used hardware description language Verilog HDL and FPGAs.
- The main features of this book include:
 - Explaining how to perform synthesis and verification to achieve optimized synthesis results and compile times.
 - Illustrating the entire design and verification flow using an FPGA case study.
 - Emphasizing design implementation trade off options with coverage of ASICs and FPGAs.
 - Providing plentiful worked examples and review questions in each section for readers to test their understanding of the related topics.
 - Giving readers deeper understanding with plentiful review questions in each section and end of chapter problems.
 - Incorporating many case studies to help the reader grasp the essentials of practical digital systems to be designed using Verilog HDL and FPGAs.
 - Highlighting Verilog HDL syntax throughout the book to facilitate readers to refer the desired syntax as they need.
 - Printing all keywords in boldface throughout the book to emphasize the language structures and improve the readability of Verilog HDL modules.

This book is the ideal textbook for the following courses: Digital System Design, FPGA System Designs and Practices, Advanced Digital Systems Design and the like. In addition, it can be used as a self studying or professional reference book in this field.

Architectures for Computer Vision Hong Jeong, 2014-08-05

This book provides comprehensive coverage of 3D vision systems from vision models and state of the art algorithms to their hardware architectures for implementation on DSPs, FPGA and ASIC chips and GPUs. It aims to fill the gaps between computer vision algorithms and real time digital circuit implementations especially with Verilog HDL design. The organization of this book is vision and hardware module directed based on Verilog vision modules. 3D vision modules, parallel vision architectures and Verilog designs for the stereo matching system with various parallel architectures. Provides Verilog vision simulators tailored to the design and testing of general vision chips. Bridges the differences between C/C++ and HDL to encompass both software realization and chip implementation. Includes numerous examples that realize vision algorithms and general vision processing in HDL. Unique in providing an organized and complete overview of how a real time 3D vision system on chip can be designed. Focuses on the digital VLSI aspects and implementation of digital signal processing tasks on hardware platforms such as ASICs and FPGAs for 3D vision systems which have not been comprehensively covered in one single book. Provides a timely view of the pervasive use of vision systems and the challenges of fusing information from different vision modules. Accompanying website includes software and HDL code packages to enhance further learning and develop advanced systems. A solution set and lecture slides are provided on the book's companion website. The book is aimed at graduate students and

researchers in computer vision and embedded systems as well as chip and FPGA designers Senior undergraduate students specializing in VLSI design or computer vision will also find the book to be helpful in understanding advanced applications

Digital Systems Design Using Verilog Charles Roth,Lizy K. John,Byeong Kil Lee,2015-01-01 DIGITAL SYSTEMS DESIGN USING VERILOG integrates coverage of logic design principles Verilog as a hardware design language and FPGA implementation to help electrical and computer engineering students master the process of designing and testing new hardware configurations A Verilog equivalent of authors Roth and John s previous successful text using VHDL this practical book presents Verilog constructs side by side with hardware encouraging students to think in terms of desired hardware while writing synthesizable Verilog Following a review of the basic concepts of logic design the authors introduce the basics of Verilog using simple combinational circuit examples followed by models for simple sequential circuits Subsequent chapters ask readers to tackle more and more complex designs Important Notice Media content referenced within the product description or the product text may not be available in the ebook version

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