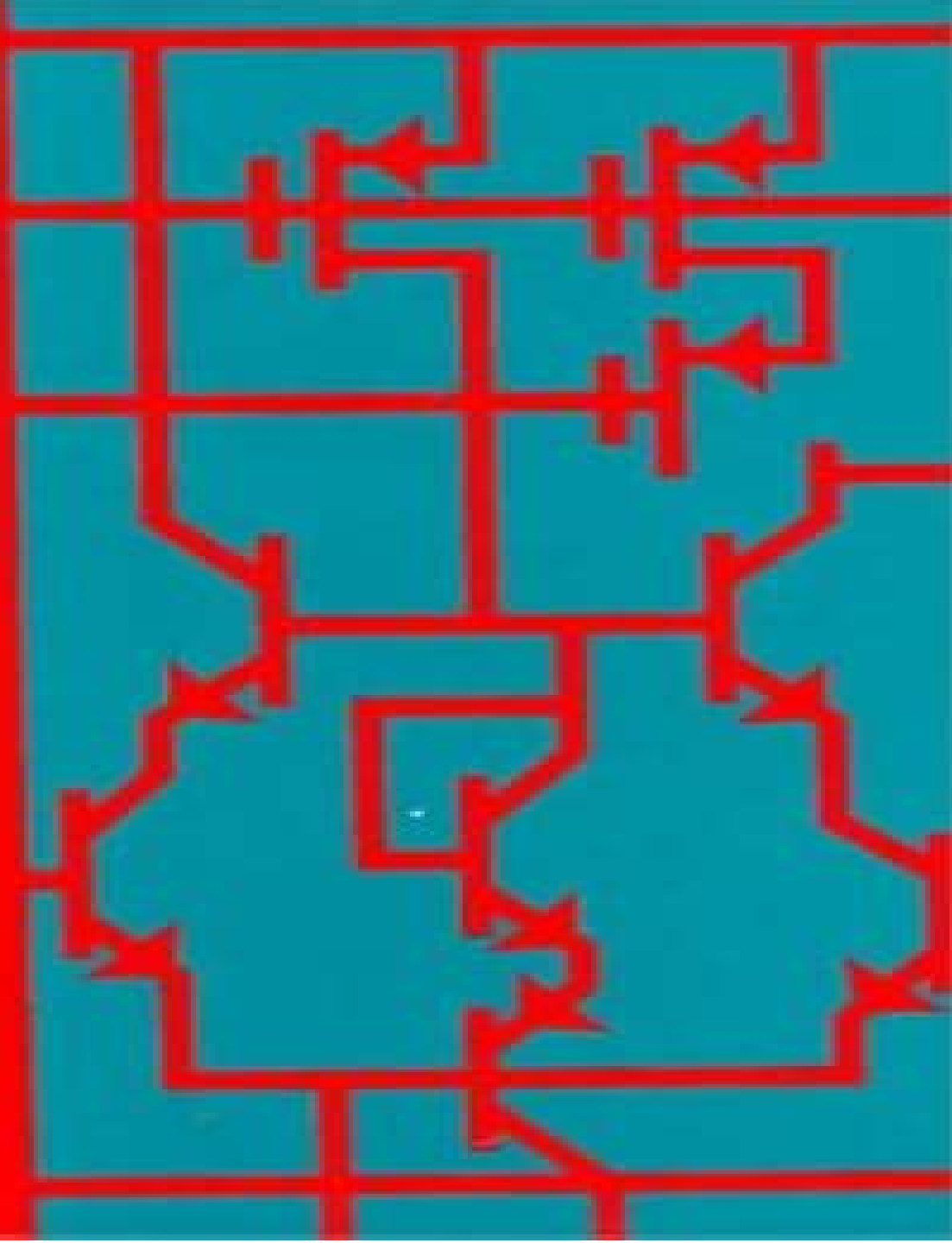


DESIGN OF ANALOG INTEGRATED CIRCUITS AND SYSTEMS

KENNETH R. LAKER
WILLY M.C. SANSEN



Design Of Analog Integrated Circuits And Systems Laker

Jean-Michel Redouté, Michiel Steyaert



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Design of Analog Integrated Circuits and Systems Kenneth R. Laker, 2002 Design of Analog Integrated Circuits and Systems Kenneth R. Laker, Willy M. C. Sansen, 1994 It follows with a thorough treatment of design operational and operational transconductance amplifiers and concludes with a unified presentation of sample data and continuous time signal processing systems Computer-Aided Design of Analog Integrated Circuits and Systems Rob A. Rutenbar, Georges G. E. Gielen, 2002-05-06 The tools and techniques you need to break the analog design bottleneck Ten years ago analog seemed to be a dead end technology Today System on Chip SoC designs are increasingly mixed signal designs With the advent of application specific integrated circuits ASIC technologies that can integrate both analog and digital functions on a single chip analog has become more crucial than ever to the design process Today designers are moving beyond hand crafted one transistor at a time methods They are using new circuit and physical synthesis tools to design practical analog circuits new modeling and analysis tools to allow rapid exploration of system level alternatives and new simulation tools to provide accurate answers for analog circuit behaviors and interactions that were considered impossible to handle only a few years ago To give circuit designers and CAD professionals a better understanding of the history and the current state of the art in the field this volume collects in one place the essential set of analog CAD papers that form the foundation of today s new analog design automation tools Areas covered are Analog synthesis Symbolic analysis Analog layout Analog modeling and analysis Specialized analog simulation Circuit centering and yield optimization Circuit testing Computer Aided Design of Analog Integrated Circuits and Systems is the cutting edge reference that will be an invaluable resource for every semiconductor circuit designer and CAD professional who hopes to break the analog design bottleneck A Computer-Aided Design and Synthesis Environment for Analog Integrated Circuits Geert Van der Plas, Georges Gielen, Willy M.C. Sansen, 2005-12-27 This text addresses the design methodologies and CAD tools available for the systematic design and design automation of analogue integrated circuits Two complementary approaches discussed increase analogue design productivity demonstrated throughout using design times of the different design experiments undertaken *Design of Low-Voltage Low-Power CMOS Delta-Sigma A/D Converters* Vincenzo Peluso, Michiel Steyaert, Willy M.C. Sansen, 2013-03-09 Design of Low Voltage Low Power CMOS Delta Sigma A D Converters investigates the feasibility of designing Delta Sigma Analog to Digital Converters for very low supply voltage lower than 1.5V and low power operation in standard CMOS processes The chosen technique of implementation is the Switched Opamp Technique which provides Switched Capacitor operation at low supply voltage without the need to apply voltage multipliers or low V_t MOST devices A method of implementing the classic single loop and cascaded Delta Sigma modulator topologies with half delay integrators is presented Those topologies are studied in order to find the parameters that maximise the performance in terms of peak SNR Based on a linear model the performance degradations of higher order single loop and cascaded modulators compared to a hypothetical

ideal modulator are quantified An overview of low voltage Switched Capacitor design techniques such as the use of voltage multipliers low VtMOST devices and the Switched Opamp Technique is given An in depth discussion of the present status of the Switched Opamp Technique covers the single ended Original Switched Opamp Technique the Modified Switched Opamp Technique which allows lower supply voltage operation and differential implementation including common mode control techniques The restrictions imposed on the analog circuits by low supply voltage operation are investigated Several low voltage circuit building blocks some of which are new are discussed A new low voltage class AB OTA especially suited for differential Switched Opamp applications together with a common mode feedback amplifier and a comparator are presented and analyzed As part of a systematic top down design approach the non ideal charge transfer of the Switched Opamp integrator cell is modeled based upon several models of the main opamp non ideal characteristics Behavioral simulations carried out with these models yield the required opamp specifications that ensure that the intended performance is met in an implementation A power consumption analysis is performed The influence of all design parameters especially the low power supply voltage is highlighted Design guidelines towards low power operation are distilled Two implementations are presented together with measurement results The first one is a single ended implementation of a Delta Sigma ADC operating with 1.5V supply voltage and consuming 100 W for a 74 dB dynamic range in a 3.4 kHz bandwidth The second implementation is differential and operates with 900 mV It achieves 77 dB dynamic range in 16 kHz bandwidth and consumes 40 W Design of Low Voltage Low Power CMOS Delta Sigma A/D Converters is essential reading for analog design engineers and researchers

Wireless Communications Circuits and Systems Institution of Electrical Engineers, 2004 This book examines integrated circuits systems and transceivers for wireless and mobile communications It covers the most recent developments in key RF/IF analogue mixed signal components and single chip transceivers in CMOS technology *Modern Analog Filter Analysis and Design* R. Raut, M. N. S. Swamy, 2011-09-22 Starting from the fundamentals the present book describes methods of designing analog electronic filters and illustrates these methods by providing numerical and circuit simulation programs The subject matters comprise many concepts and techniques that are not available in other text books on the market To name a few principle of transposition and its application in directly realizing current mode filters from well known voltage mode filters an insight into the technological aspect of integrated circuit components used to implement an integrated circuit filter a careful blending of basic theory numerical verification using MATLAB and illustration of the actual circuit behaviour using circuit simulation program SPICE illustration of few design cases using CMOS and BiCMOS technological processes *EMC of Analog Integrated Circuits* Jean-Michel Redouté, Michiel Steyaert, 2009-10-10

Environmental electromagnetic pollution has drastically increased over the last decades The omnipresence of communication systems various electronic appliances and the use of ever increasing frequencies all contribute to a noisy electromagnetic environment which acts detrimentally on sensitive electronic equipment Integrated circuits must be able to operate

satisfactorily while cohabiting harmoniously in the same appliance and not generate intolerable levels of electromagnetic emission while maintaining a sound immunity to potential electromagnetic disturbances analog integrated circuits are in particular more easily disturbed than their digital counterparts since they don't have the benefit of dealing with predefined levels ensuring an innate immunity to disturbances The objective of the research domain presented in EMC of Analog Integrated Circuits is to improve the electromagnetic immunity of considered analog integrated circuits so that they start to fail at relevantly higher conduction levels than before

Analog Integrated Circuit Design Tony Chan Carusone, David Johns, Kenneth Martin, 2011-12-13 When first published in 1996 this text by David Johns and Kenneth Martin quickly became a leading textbook for the advanced course on Analog IC Design This new edition has been thoroughly revised and updated by Tony Chan Carusone a University of Toronto colleague of Drs Johns and Martin Dr Chan Carusone is a specialist in analog and digital IC design in communications and signal processing This edition features extensive new material on CMOS IC device modeling processing and layout Coverage has been added on several types of circuits that have increased in importance in the past decade such as generalized integer N phase locked loops and their phase noise analysis voltage regulators and 1.5b per stage pipelined A/D converters Two new chapters have been added to make the book more accessible to beginners in the field frequency response of analog ICs and basic theory of feedback amplifiers

Continuous-Time Low-Pass Filters for Integrated Wideband Radio Receivers Ville Saari, Jussi Ryyänen, Saska Lindfors, 2012-03-15 This book presents a new filter design approach and concentrates on the circuit techniques that can be utilized when designing continuous time low pass filters in modern ultra deep submicron CMOS technologies for integrated wideband radio receivers Coverage includes system level issues related to the design and implementation of a complete single chip radio receiver and related to the design and implementation of a filter circuit as a part of a complete single chip radio receiver Presents a new filter design approach emphasizing low voltage circuit solutions that can be implemented in modern ultra deep submicron CMOS technologies Includes filter circuit implementations designed as a part of a single chip radio receiver in modern 1.2V 0.13μm and 65nm CMOS Describes design and implementation of a continuous time low pass filter for a multicarrier WCDMA base station Emphasizes system level considerations throughout

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