

Digital Design

with RTL Design, VHDL, and Verilog

SECOND EDITION



Frank Vahid

Digital Design With Rtl Design Verilog And Vhdl

Frank Vahid



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Taraate, 2021-10-31 This second edition focuses on the thought process of digital design and implementation in the context of VLSI and system design It covers the Verilog 2001 and Verilog 2005 RTL design styles constructs and the optimization at the RTL and synthesis level The book also covers the logic synthesis low power multiple clock domain design concepts and design performance improvement techniques The book includes 250 design examples illustrations and 100 exercise questions This volume can be used as a core or supplementary text in undergraduate courses on logic design and as a text for professional and vocational coursework In addition it will be a hands on professional reference and a self study aid for hobbyists *Digital Logic Design Using Verilog* Vaibbhav Taraate, 2016-05-17 This book is designed to serve as a hands on professional reference with additional utility as a textbook for upper undergraduate and some graduate courses in digital logic design This book is organized in such a way that that it can describe a number of RTL design scenarios from simple to

complex The book constructs the logic design story from the fundamentals of logic design to advanced RTL design concepts Keeping in view the importance of miniaturization today the book gives practical information on the issues with ASIC RTL design and how to overcome these concerns It clearly explains how to write an efficient RTL code and how to improve design performance The book also describes advanced RTL design concepts such as low power design multiple clock domain design and SOC based design The practical orientation of the book makes it ideal for training programs for practicing design engineers and for short term vocational programs The contents of the book will also make it a useful read for students and hobbyists

Heterogeneous SoC Design and Verification Khaled Salah Mohamed, 2024-03-22 This book covers the foundations of hardware software codesign on chip communication debugging and verification for heterogeneous SoCs Its primary objective is to empower designers in making informed decisions guiding them to strike the perfect balance between flexibility and performance for their SoC designs Readers will benefit from a detailed exploration of the essential elements of the hardware and software codesign framework accompanied by a discussion of the driving motivations behind this approach The author also provides an in depth review of various hardware design architectures shedding light on different design possibilities Furthermore the book presents key concepts concerning hardware and software communication unraveling the intricate interactions within an SoC This book provides a holistic introduction to the methodologies underpinning SoC design and verification making it an indispensable companion for both novice and experienced designers navigating the ever evolving landscape of hardware software codesign

Principles of VLSI RTL Design Sanjay Churiwala, Sapan Garg, 2011-05-04 Since register transfer level RTL design is less about being a bright engineer and more about knowing the downstream implications of your work this book explains the impact of design decisions taken that may give rise later in the product lifecycle to issues related to testability data synchronization across clock domains synthesizability power consumption routability etc all which are a function of the way the RTL was originally written Readers will benefit from a highly practical approach to the fundamentals of these topics and will be given clear guidance regarding necessary safeguards to observe during RTL design

Digital Design from the VLSI Perspective Vaibhav Taraate, 2022-10-02 This volume covers digital design techniques exercises and applications The book discusses digital design and implementation in the context of VLSI and embedded system design It covers basic digital design techniques to high speed design techniques The contents also cover performance improvement optimization concepts and design case studies It includes pedagogical features such as design examples and illustrations This book will be a useful guide for hardware engineers logic design engineers professionals and hobbyists looking to learn and use the digital design to develop VLSI based algorithms architectures and products

Smart Embedded Systems and Applications Saad Motahhir, 2023-02-20 This book covers a wide range of challenges technologies and state of the art for the design development and realization of smart and complex embedded systems and their applications i e software and hardware development with the use of digital technologies and

quality assurance for critical applications This book starts with automotive safety systems which is one of the major functional domains It discusses the importance of software in automotive systems followed by an insight into Automotive Software Standards ISO26262 and Autosar The book further discusses the use of Processor in the loop test for an adaptive trajectory tracking control for quadrotor UAVs It also illustrates the role of embedded systems in medical engineering Various innovative applications involving the concept of image processing and Internet of Things are also presented in this book The SoC Power Estimation is also investigated Finally a Review of the Hardware Software Partitioning Algorithms with some future works have been presented this book is intended for academicians researchers and industrialists

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