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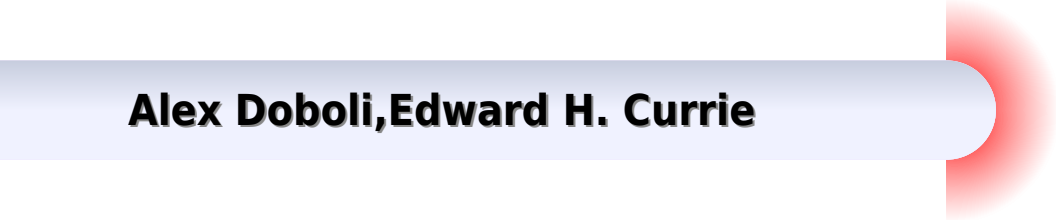


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Co Verification Of Hardware And Software For Arm Soc Design Embedded Technology

Alex Doboli, Edward H. Currie



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Co-verification of Hardware and Software for ARM SoC Design Jason Andrews, 2004-09-04 Hardware software co verification is how to make sure that embedded system software works correctly with the hardware and that the hardware has been properly designed to run the software successfully before large sums are spent on prototypes or manufacturing This is the first book to apply this verification technique to the rapidly growing field of embedded systems on a chip SoC As traditional embedded system design evolves into single chip design embedded engineers must be armed with the necessary information to make educated decisions about which tools and methodology to deploy SoC verification requires a mix of expertise from the disciplines of microprocessor and computer architecture logic design and simulation and C and Assembly language embedded software Until now the relevant information on how it all fits together has not been available Andrews a recognized expert provides in depth information about how co verification really works how to be successful using it and pitfalls to avoid He illustrates these concepts using concrete examples with the ARM core a technology that has the dominant market share in embedded system product design The companion CD ROM contains all source code used in the design examples a searchable e book version and useful design tools The only book on verification for systems on a chip SoC on the market Will save engineers and their companies time and money by showing them how to speed up the testing process while still avoiding costly mistakes Design examples use the ARM core the dominant technology in SoC and all the source code is included on the accompanying CD Rom so engineers can easily use it in their own designs

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applications e.g automotive where failures are unacceptable Since the verification of complex systems needs to encompass the verification of both hardware and embedded software modules this book focuses on verification and debugging approaches for embedded software with hardware dependencies Coverage includes the entire flow of design verification and debugging of embedded software and all key approaches to debugging dynamic static and hybrid verification This book discusses the current industrial embedded software verification flow as well as emerging trends with focus on formal and hybrid verification and debugging approaches

Computer Engineering and Technology Weixia Xu, Liquan Xiao, Pingjing Lu, Jinwen Li, Chengyi Zhang, 2013-01-03 This book constitutes the refereed proceedings of the 16th National Conference on Computer Engineering and Technology NCCET 2012 held in Shanghai China in August 2012 The 27 papers presented were carefully reviewed and selected from 108 submissions They are organized in topical sections named microprocessor and implementation design of integration circuit I O interconnect and measurement verification and others

The Functional Verification of Electronic Systems Brian Bailey, 2005-01-30 Addressing the need for full and accurate functional information during the design process this guide offers a comprehensive overview of functional verification from the points of view of leading experts at work in the electronic design industry

Introduction to Mixed-Signal, Embedded Design Alex Doboli, Edward H. Currie, 2010-12-17 This textbook is written for junior senior undergraduate and first year graduate students in the electrical and computer engineering departments Using PSoC mixed signal array design the authors define the characteristics of embedd design embedded mixed signal architectures and top down design Optimized implementations of these designs are included to illustrate the theory Exercises are provided at the end of each chapter for practice Topics covered include the hardware and software used to implement analog and digital interfaces various filter structures amplifiers and other signal conditioning circuits pulse width modulators timers and data structures for handling multiple similar peripheral devices The practical exercises contained in the companion laboratory manual which was co authored by Cypress Staff Applications Engineer Dave Van Ess are also based on PSoC PSoC s integrated microcontroller highly configurable analog digital peripherals and a full set of development tools make it an ideal learning tool for developing mixed signal embedded design skills

Heterogeneous SoC Design and Verification Khaled Salah Mohamed, 2024-03-22 This book covers the foundations of hardware software codesign on chip communication debugging and verification for heterogenous SoCs Its primary objective is to empower designers in making informed decisions guiding them to strike the perfect balance between flexibility and performance for their SoC designs Readers will benefit from a detailed exploration of the essential elements of the hardware and software codesign framework accompanied by a discussion of the driving motivations behind this approach The author also provides an in depth review of various hardware design architectures shedding light on different design possibilities Furthermore the book presents key concepts concerning hardware and software communication unraveling the intricate interactions within an SoC This book provides a holistic introduction to the

methodologies underpinning SoC design and verification making it an indispensable companion for both novice and experienced designers navigating the ever evolving landscape of hardware software codesign System-on-a-Chip Verification Prakash Rashinkar, Peter Paterson, Leena Singh, 2007-05-08 System On a Chip Verification Methodology and Techniques is the first book to cover verification strategies and methodologies for SOC verification from system level verification to the design sign off The topics covered include Introduction to the SOC design and verification aspects System level verification in brief Block level verification Analog mixed signal simulation Simulation HW SW Co verification Static netlist verification Physical verification and Design sign off in brief All the verification aspects are illustrated with a single reference design for Bluetooth application System On a Chip Verification Methodology and Techniques takes a systematic approach that covers the following aspects of verification strategy in each chapter Explanation of the objective involved in performing verification after a given design step Features of options available When to use a particular option How to select an option and Limitations of the option This exciting new book will be of interest to all designers and test professionals

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