



DESIGN OF 3D INTEGRATED CIRCUITS AND SYSTEMS

EDITED BY

Rohit Sharma



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Design Of 3d Integrated Circuits And Systems Devices Circuits And Systems

Hao Yu, Chuan Seng Tan



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Physical Design for 3D Integrated Circuits Aida Todri-Sanial, Chuan Seng Tan, 2017-12-19 Physical Design for 3D Integrated Circuits reveals how to effectively and optimally design 3D integrated circuits ICs. It also analyzes the design tools for 3D circuits while exploiting the benefits of 3D technology. The book begins by offering an overview of physical design challenges with respect to conventional 2D circuits and then each chapter delivers an in-depth look at a specific physical design topic. This comprehensive reference contains extensive coverage of the physical design of 2.5D, 3D ICs and monolithic 3D ICs. Supplies state-of-the-art solutions for challenges unique to 3D circuit design. Features contributions from renowned experts in their respective fields. Physical Design for 3D Integrated Circuits provides a single convenient source of cutting-edge information for those pursuing 2.5D, 3D technology.

Advances In 3D Integrated Circuits And Systems Hao Yu, Chuan Seng Tan, 2015-08-28 3D integration is an emerging technology for the design of many core microprocessors and memory integration. This book, *Advances in 3D Integrated Circuits and Systems*, is written to help readers understand 3D integrated circuits in three stages: device basics, system-level management, and real designs. Contents presented in this book include fabrication techniques for 3D TSV and 2.5D TSI, device modeling, physical designs, thermal power, and I/O management, and 3D designs of sensors, I/Os, multi-core processors, and memory. Advanced undergraduates, graduate students, researchers, and engineers may find this text useful for understanding the many challenges faced in the development and building of 3D integrated circuits and systems.

CMOS Time-Mode Circuits and Systems Fei Yuan, 2018-09-03 Time-mode circuits, where information is represented by time difference between digital events, offer a viable and technology-friendly means to realize mixed-mode circuits and systems in nanometer complementary metal-oxide-semiconductor (CMOS) technologies. Various architectures of time-based signal processing and design techniques of CMOS time-mode circuits have emerged; however, an in-depth examination of the principles of time-based signal processing and design techniques of time-mode circuits has not been available until now. *CMOS Time-Mode Circuits and Systems: Fundamentals and Applications* is the first book to deliver a comprehensive treatment of CMOS time-mode circuits and systems. Featuring contributions from leading experts, this authoritative text contains a rich collection of literature on time-mode circuits and systems. The book begins by presenting a critical comparison of voltage-mode, current-mode, and time-mode signaling for mixed-mode signal processing and then covers the fundamentals of time-mode signal processing, such as voltage-to-time converters, all-digital phase-locked loops, and frequency synthesizers. It investigates the performance characteristics, architecture, design techniques, and implementation of time-to-digital converters. It discusses time-mode delta-sigma-based analog-to-digital converters, placing a great emphasis on time-mode quantizers. It includes a detailed study of ultra-low-power integrated time-mode temperature measurement systems. *CMOS Time-Mode Circuits and Systems: Fundamentals and Applications* provides a valuable reference for circuit design engineers, hardware system engineers, graduate students, and

others seeking to master this fast evolving field *Optical Fiber Sensors* Ginu Rajan,2017-12-19 *Optical Fiber Sensors Advanced Techniques and Applications* describes the physical principles of and latest developments in optical fiber sensors Providing a fundamental understanding of the design operation and practical applications of fiber optic sensing systems this book Discusses new and emerging areas of research including photonic crystal fiber sensors micro and nanofiber sensing liquid crystal photonics acousto optic effects in fiber and fiber laser based sensing Covers well established areas such as surface plasmon resonance sensors interferometric fiber sensors polymer fiber sensors Bragg gratings in polymer and silica fibers and distributed fiber sensors Explores humidity sensing applications smart structure applications and medical applications supplying detailed examples of the various fiber optic sensing technologies in use *Optical Fiber Sensors Advanced Techniques and Applications* draws upon the extensive academic and industrial experience of its contributing authors to deliver a comprehensive introduction to optical fiber sensors with a strong practical focus suitable for undergraduate and graduate students as well as scientists and engineers working in the field **Metallic Spintronic Devices** Xiaobin Wang,2017-12-19 *Metallic Spintronic Devices* provides a balanced view of the present state of the art of metallic spintronic devices addressing both mainstream and emerging applications from magnetic tunneling junction sensors and spin torque oscillators to spin torque memory and logic Featuring contributions from well known and respected industrial and academic experts this cutting edge work not only presents the latest research and developments but also Describes spintronic applications in current and future magnetic recording devices Discusses spin transfer torque magnetoresistive random access memory STT MRAM device architectures and modeling Explores prospects of STT MRAM scaling such as detailed multilevel cell structure analysis Investigates spintronic device write and read optimization in light of spintronic memristive effects Considers spintronic research directions based on yttrium iron garnet thin films including spin pumping magnetic proximity spin hall and spin Seebeck effects Proposes unique solutions for low power spintronic device applications where memory is closely integrated with logic *Metallic Spintronic Devices* aims to equip anyone who is serious about metallic spintronic devices with up to date design modeling and processing knowledge It can be used either by an expert in the field or a graduate student in course curriculum **Noise Coupling in System-on-Chip** Thomas Noulis,2018-01-09 *Noise Coupling* is the root cause of the majority of Systems on Chip SoC product fails The book discusses a breakthrough substrate coupling analysis flow and modelling toolset addressing the needs of the design community The flow provides capability to analyze noise components propagating through the substrate the parasitic interconnects and the package Using this book the reader can analyze and avoid complex noise coupling that degrades RF and mixed signal design performance while reducing the need for conservative design practices With chapters written by leading international experts in the field novel methodologies are provided to identify noise coupling in silicon It additionally features case studies that can be found in any modern CMOS SoC product for mobile communications automotive applications and readout front

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