

Stéphane Badel · Can Baltaci
Alessandro Cevrero · Yusuf Leblebici

Design Automation for Differential MOS Current-Mode Logic Circuits

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Stéphane Badel, Yusuf Leblebici



Design Automation For Differential Mos Current Mode Logic Circuits:

Design Automation for Differential MOS Current-Mode Logic Circuits Stéphane Badel, Can Baltaci, Alessandro Cevrero, Yusuf Leblebici, 2018-07-02 This book discusses the implementation of digital circuits by using MCML gates. Although digital circuit implementation is possible with other elements such as CMOS gates, MCML implementations can provide superior performance in certain applications. This book provides a complete automation methodology for the implementation of digital circuits in MCML and provides an extensive explanation on the technical details of design of MCML. A systematic methodology is presented to build efficient MCML standard cell libraries and a complete top down design flow is shown to implement complex systems using such building blocks. *Design Automation for Differential MOS Current-Mode Logic Circuits* Stéphane Badel, Yusuf Leblebici, 2016-01-05 Design Automation for Differential MOS Current Mode Logic Circuits provides a very detailed and comprehensive analysis of fundamental MCML circuits, their design and performance optimization under competing constraints such as output drive capability, power dissipation, noise margin and silicon area. A systematic methodology is presented to build efficient MCML standard cell libraries and a complete top down design flow is shown to implement complex systems using such building blocks. As such, Design Automation for Differential MOS Current Mode Logic Circuits presents one of the first ever top down design approaches using differential cells for high performance and low noise operation. Advanced Circuits for Emerging Technologies Krzysztof Iniewski, 2012-04-17 The book will address the state of the art in integrated circuit design in the context of emerging systems. New exciting opportunities in body area networks, wireless communications, data networking and optical imaging are discussed. Emerging materials that can take system performance beyond standard CMOS, like Silicon on Insulator (SOI), Silicon Germanium (SiGe) and Indium Phosphide (InP) are explored. Three dimensional 3 D CMOS integration and co integration with sensor technology are described as well. The book is a must for anyone serious about circuit design for future technologies. The book is written by top notch international experts in industry and academia. The intended audience is practicing engineers with integrated circuit background. The book will be also used as a recommended reading and supplementary material in graduate course curriculum. Intended audience is professionals working in the integrated circuit design field. Their job titles might be design engineer, product manager, marketing manager, design team leader etc. The book will be also used by graduate students. Many of the chapter authors are University Professors. **Model and Design of Improved Current Mode Logic Gates** Kirti Gupta, Neeta Pandey, Maneesha Gupta, 2019-11-22 This book presents MOSFET based current mode logic (CML) topologies which increase the speed and lower the transistor count, supply voltage and power consumption. The improved topologies modify the conventional PDN load and the current source sections of the basic CML gates. Electronic system implementation involves embedding digital and analog circuits on a single die, shifting towards mixed mode circuit design. The high resolution, low power and low voltage analog circuits are combined with high frequency complex digital circuits and the conventional

static CMOS logic generates large current spikes during the switching also referred to as digital switching noise which degrade the resolution of the sensitive analog circuits via supply line and substrate coupling This problem is exacerbated further with scaling down of CMOS technology due to higher integration levels and operating frequencies In the literature several methods are described to reduce the propagation of the digital switching noise However in high resolution applications these methods are not sufficient The conventional CMOS static logic is no longer an effective solution and therefore an alternative with reduced current spikes or that draws a constant supply current must be selected The current mode logic CML topology with its unique property of requiring constant supply current is a promising alternative to the conventional CMOS static logic

Cryptographic Hardware and Embedded Systems - CHES 2009 Christophe Clavier, Kris Gaj, 2009-08-28 CHES 2009 the 11th workshop on Cryptographic Hardware and Embedded Systems was held in Lausanne Switzerland September 6-9 2009 The workshop was sponsored by the International Association for Cryptologic Research IACR The workshop attracted a record number of 148 submissions from 29 countries of which the Program Committee selected 29 for publication in the workshop proceedings resulting in an acceptance rate of 19.6% the lowest in the history of CHES The review process followed strict standards each paper received at least four reviews and some as many as eight reviews Members of the Program Committee were restricted to co-authoring at most two submissions and their papers were evaluated by an extended number of reviewers The Program Committee included 53 members representing 20 countries and five continents These members were carefully selected to represent academia industry and government as well as to include world class experts in various research fields of interest to CHES The Program Committee was supported by 148 external reviewers The total number of people contributing to the review process including Program Committee members external reviewers and Program Co-chairs exceeded 200 The papers collected in this volume represent cutting edge worldwide research in the rapidly growing and evolving area of cryptographic engineering

The Electronic Design Automation Handbook Dirk Jansen, 2010-02-23 When I attended college we studied vacuum tubes in our junior year At that time an average radio had five vacuum tubes and better ones even seven Then transistors appeared in the 1960s A good radio was judged to be one with more than ten transistors Later good radios had 15-20 transistors and after that everyone stopped counting transistors Today modern processors running personal computers have over 10 million transistors and more millions will be added every year The difference between 10 and 20M is in complexity methodology and business models Designs with 10 transistors are easily generated by design engineers without any tools whilst designs with 20M transistors can not be done by humans in reasonable time without the help of Prof Dr Gajski demonstrates the Y chart automation This difference in complexity introduced a paradigm shift which required sophisticated methods and tools and introduced design automation into design practice By the decomposition of the design process into many tasks and abstraction levels the methodology of designing chips or systems has also evolved Similarly the business model has changed

from vertical integration in which one company did all the tasks from product specification to manufacturing to globally distributed client server production in which most of the design and manufacturing tasks are outsourced

Applications of Computing, Automation and Wireless Systems in Electrical Engineering Sukumar Mishra, Yog Raj Sood, Anuradha Tomar, 2019-05-31 This book discusses key concepts challenges and potential solutions in connection with established and emerging topics in advanced computing renewable energy and network communications Gathering edited papers presented at MARC 2018 on July 19 2018 it will help researchers pursue and promote advanced research in the fields of electrical engineering communication computing and manufacturing

High-speed Optical Transceivers: Integrated Circuits Designs And Optical Devices Techniques Yuyu Liu, Huazhong Yang, 2006-03-09 This book explores the unique advantages and large inherent transmission capacity of optical fiber communication systems The long term and high risk research challenges of optical transceivers are analyzed with a view to sustaining the seemingly insatiable demand for bandwidth A broad coverage of topics relating to the design of high speed optical devices and integrated circuits oriented to low power low cost and small area is discussed Written by specialists with many years of research and engineering experience in the field of optical fiber communication this book is essential for an audience dedicated to the development of integrated electronic systems for optical communication applications It can also be used as a supplementary text for graduate courses on optical transceiver IC design

Advancing VLSI through Machine Learning Abhishek Narayan Tripathi, Jagana Bihari Padhy, Indrasen Singh, Shubham Tayal, Ghanshyam Singh, 2025-03-31 This book explores the synergy between very large scale integration VLSI and machine learning ML and its applications across various domains It investigates how ML techniques can enhance the design and testing of VLSI circuits improve power efficiency optimize layouts and enable novel architectures This book bridges the gap between VLSI and ML showcasing the potential of this integration in creating innovative electronic systems advancing computing capabilities and paving the way for a new era of intelligent devices and technologies Additionally it covers how VLSI technologies can accelerate ML algorithms enabling more efficient and powerful data processing and inference engines It explores both hardware and software aspects covering topics like hardware accelerators custom hardware for specific ML tasks and ML driven optimization techniques for chip design and testing This book will be helpful for academicians researchers postgraduate students and those working in ML driven VLSI

Secure Integrated Circuits and Systems Ingrid M.R. Verbauwhede, 2010-04-05 On any advanced integrated circuit or system on chip there is a need for security In many applications the actual implementation has become the weakest link in security rather than the algorithms or protocols The purpose of the book is to give the integrated circuits and systems designer an insight into the basics of security and cryptography from the implementation point of view As a designer of integrated circuits and systems it is important to know both the state of the art attacks as well as the countermeasures Optimizing for security is different from optimizations for speed area or power consumption It is therefore difficult to attain the delicate balance between the extra

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web aug 19 2019 aphrodite greek goddess of love miguel navarro stone getty images aphrodite is the greek goddess of beauty love and sexuality she is sometimes known as the cyprian because there was a cult center of aphrodite on cyprus aphrodite is the mother of the god of love eros she is the wife of the ugliest of the gods hephaestus

12 main greek gods of greek mythology - Oct 05 2022

web nov 26 2019 zeus zeus is the greek god of the skies his brothers are poseidon and hades poseidon is one of the twelve olympians while hades is not zeus had three sisters hestia demeter and hera zeus is considered the king of all the gods 2 poseidon poseidon is the greek god of the seas

list of greek mythological figures encyclopedia britannica - Sep 04 2022

web this is an alphabetically ordered list of greek mythological figures including deities and other immortal beings as well as significant legendary mortals

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web zethus mortal son of zeus and antiope brother of amphon zeus olympian god son of cronus and rhea greek god of heaven and earth zeuxippe naiad nymph daughter of eridanos wife of pandion mother of procne philomela erechtheus and butes nymph of the well of athens

greek gods list names of the greek gods greek gods and goddesses - Apr 11 2023

web this page is a list of the names of greek gods in ancient mythology and their roles it will be continually updated with additions corrections and more information on each of the gods achelous the patron god of the silver swirling achelous river aeolus greek god of the winds and air aether

list of greek mythological figures wikipedia - Jul 14 2023

web major gods and goddesses deity description aphrodite Ἀφροδίτη aphroditē goddess of beauty love desire and pleasure in hesiod s theogony 188 206 she was born from sea foam and the severed genitals of uranus in homer s iliad 5 370 417 she is daughter of zeus and dione

greek gods and goddesses a z myth guide - Aug 15 2023

web jun 16 2018 the names of greek gods and goddesses mentioned in this list will probably surprise a few readers even those of you who think you re well versed in the greek myths it s not all about the twelve olympian gods in actual fact there

are a huge number of deities linked to the greek myths

zeus myths wife children facts britannica - Nov 06 2022

web sep 5 2023 zeus in ancient greek religion chief deity of the pantheon a sky and weather god who was identical with the roman god jupiter he was regarded as the sender of thunder and lightning rain and winds and his traditional weapon was the thunderbolt zeus was called the father of both gods and men