

Kenichi Okada · Shouhei Kousai
Editors

Digitally-Assisted Analog and RF CMOS Circuit Design for Software-Defined Radio

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Digitally Assisted Analog And Rf Cmos Circuit Design For Software Defined Radio

**Robert Fasthuber, Francky
Cathoor, Praveen Raghavan, Frederik
Naessens**

Digitally Assisted Analog And Rf Cmos Circuit Design For Software Defined Radio:

Digitally-Assisted Analog and RF CMOS Circuit Design for Software-Defined Radio Kenichi Okada, Shouhei Kousai, 2011-08-26 This book describes the state of the art in RF analog and mixed signal circuit design for Software Defined Radio SDR. It synthesizes for analog RF circuit designers the most important general design approaches to take advantage of the most recent CMOS technology which can integrate millions of transistors as well as several real examples from the most recent research results.

VLSI Design and Test for Systems Dependability Shojiro Asai, 2018-07-20 This book discusses the new roles that the VLSI very large scale integration of semiconductor circuits is taking for the safe, secure, and dependable design and operation of electronic systems. The book consists of three parts. Part I as a general introduction to this vital topic describes how electronic systems are designed and tested with particular emphasis on dependability engineering where the simultaneous assessment of the detrimental outcome of failures and cost of their containment is made. This section also describes the related research project Dependable VLSI Systems in which the editor and authors of the book were involved for 8 years. Part II addresses various threats to the dependability of VLSIs as key systems components including time dependent degradations, variations in device characteristics, ionizing radiation, electromagnetic interference, design errors, and tampering, with discussion of technologies to counter those threats. Part III elaborates on the design and test technologies for dependability in such applications as control of robots and vehicles, data processing and storage in a cloud environment, and heterogeneous wireless telecommunications. This book is intended to be used as a reference for engineers who work on the design and testing of VLSI systems with particular attention to dependability. It can be used as a textbook in graduate courses as well. Readers interested in dependable systems from social and industrial economic perspectives will also benefit from the discussions in this book.

Silicon Photonics III Lorenzo Pavesi, David J. Lockwood, 2016-01-08 This book is volume III of a series of books on silicon photonics. It reports on the development of fully integrated systems where many different photonics components are integrated together to build complex circuits. This is the demonstration of the full potentiality of silicon photonics. It contains a number of chapters written by engineers and scientists of the main companies, research centers, and universities active in the field. It can be of use for all those persons interested to know the potentialities and the recent applications of silicon photonics both in microelectronics, telecommunication, and consumer electronics market.

Stochastic Process Variation in Deep-Submicron CMOS Amir Zjajo, 2013-11-19 One of the most notable features of nanometer scale CMOS technology is the increasing magnitude of variability of the key device parameters affecting performance of integrated circuits. The growth of variability can be attributed to multiple factors including the difficulty of manufacturing control, the emergence of new systematic variation generating mechanisms, and most importantly, the increase in atomic scale randomness where device operation must be described as a stochastic process. In addition to wide sense stationary stochastic device variability and temperature variation, existence of non stationary stochastic electrical noise

associated with fundamental processes in integrated circuit devices represents an elementary limit on the performance of electronic circuits In an attempt to address these issues Stochastic Process Variation in Deep Submicron CMOS Circuits and Algorithms offers unique combination of mathematical treatment of random process variation electrical noise and temperature and necessary circuit realizations for on chip monitoring and performance calibration The associated problems are addressed at various abstraction levels i e circuit level architecture level and system level It therefore provides a broad view on the various solutions that have to be used and their possible combination in very effective complementary techniques for both analog mixed signal and digital circuits The feasibility of the described algorithms and built in circuitry has been verified by measurements from the silicon prototypes fabricated in standard 90 nm and 65 nm CMOS technology

Energy-Efficient Communication Processors Robert Fasthuber, Francky Catthoor, Praveen Raghavan, Frederik Naessens, 2013-05-29 This book describes a new design approach for energy efficient Domain Specific Instruction set Processor DSIP architectures for the wireless baseband domain The innovative techniques presented enable co design of algorithms architectures and technology for efficient implementation of the most advanced technologies To demonstrate the feasibility of the author s design approach case studies are included for crucial functionality of advanced wireless systems with increased computational performance flexibility and reusability Designers using this approach will benefit from reduced development product costs and greater scalability to future process technology nodes **Brain-Machine Interface** Amir Zjajo, 2016-03-30 This book provides a complete overview of significant design challenges in respect to circuit miniaturization and power reduction of the neural recording system along with circuit topologies architecture trends and post silicon circuit optimization algorithms The introduced novel circuits for signal conditioning quantization and classification as well as system configurations focus on optimized power per area performance from the spatial resolution i e number of channels feasible wireless data bandwidth and information quality to the delivered power of implantable system **CMOSET 2014 Vol. 2:**

Circuits and Systems Track CMOS Emerging Technologies Research, 2014-06-19 Presentation slides from the Plenary track at the 2014 CMOS Emerging Technologies Research conference in Grenoble France **Charge-based CMOS Digital RF Transmitters** Pedro Emiliano Paro Filho, Jan Craninckx, Piet Wambacq, Mark Ingels, 2016-09-27 This book introduces a completely novel architecture that can relax the trade off existing today between noise power and area consumption in a very suitable solution for advanced wireless communication systems Through the combination of charge domain operation with incremental signaling this architecture gives the best of both worlds providing the reduced area and high portability of digital intensive architectures with an improved out of band noise performance given by intrinsic noise filtering capabilities Readers will be enabled to design higher performance radio front ends that consume less power and area especially with respect to the transmitter and power amplifier designs considered by many the battery killers on most mobile devices

Multi-Mode / Multi-Band RF Transceivers for Wireless Communications Gernot Hueber, Robert Bogdan

Staszewski,2011-02-22 Summarizes cutting edge physical layer technologies for multi mode wireless RF transceivers Includes original contributions from distinguished researchers and professionals Covers cutting edge physical layer technologies for multi mode wireless RF transceivers Contributors are all leading researchers and professionals in this field

Digital Front-End in Wireless Communications and Broadcasting Fa-Long Luo,2011-09-29 Covering everything from signal processing algorithms to integrated circuit design this complete guide to digital front end is invaluable for professional engineers and researchers in the fields of signal processing wireless communication and circuit design Showing how theory is translated into practical technology it covers all the relevant standards and gives readers the ideal design methodology to manage a rapidly increasing range of applications Step by step information for designing practical systems is provided with a systematic presentation of theory principles algorithms standards and implementation Design trade offs are also included as are practical implementation examples from real world systems A broad range of topics is covered including digital pre distortion DPD digital up conversion DUC digital down conversion DDC and DC offset calibration Other important areas discussed are peak to average power ratio PAPR reduction crest factor reduction CFR pulse shaping image rejection digital mixing delay gain imbalance compensation error correction noise shaping numerical controlled oscillator NCO and various diversity methods

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