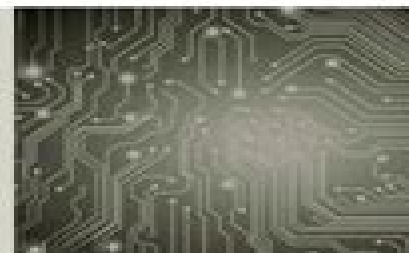


DIGITAL SYSTEMS DESIGN USING VHDL®



Third Edition

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Lizy Kurian John, Charles Roth



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Digital Systems Design Using VHDL Charles H. Roth, Lizy K. John, 2008 This textbook is intended for a senior level course in digital systems design The book covers both basic principles of digital systems design and the use of a hardware description language VHDL in the design process

Digital System Design Using VHDL Prof. Mrunalini U. Buradkar, 2024-02-09 Digital System Design Using VHDL is a comprehensive and pragmatic manual that clarifies the complex realm of digital systems by utilizing the robust hardware description language VHDL The book was written with an instructional focus targeting individuals who are engineers students or professionals who desire a thorough comprehension of VHDL and its utilization in the development of intricate electronic circuits Commencing with a comprehensive exposition of the syntax and semantics of VHDL the book guarantees that readers acquire a firm comprehension of the language s complexities Advancing beyond foundational principles it adeptly amalgamates theoretical notions with tangible instances from the real world thereby demonstrating the practical implementation of VHDL in the realm of digital system design The publication places considerable importance on experiential learning as evidenced by the varied exercises case studies and design projects that furnish readers with sufficient chances to strengthen their abilities and cultivate a high level of proficiency in VHDL The book not only addresses foundational principles but also explores more complex subjects including synthesis verification and FPGA implementation As a result it serves as a valuable resource for individuals who desire to further explore the subject matter Digital System Design Using VHDL provides readers with the necessary knowledge and skills to address current challenges in the dynamic domain of digital system design through its project oriented methodology

Introduction to Logic Circuits & Logic Design with VHDL Brock J. LaMeres, 2016-09-15 This textbook introduces readers to the fundamental hardware used in modern computers The only pre requisite is algebra so it can be taken by college freshman or sophomore students or even used in Advanced Placement courses in high school This book presents both the classical approach to digital system design i e pen and paper in addition to the modern hardware description language HDL design approach computer based This textbook enables readers to design digital systems using the modern HDL approach while ensuring they have a solid foundation of knowledge of the underlying hardware and theory of their designs This book is designed to match the way the material is actually taught in the classroom Topics are presented in a manner which builds foundational knowledge before moving onto advanced topics The author has designed the content with learning goals and assessment at its core Each section addresses a specific learning

outcome that the learner should be able to do after its completion The concept checks and exercise problems provide a rich set of assessment tools to measure learner performance on each outcome This book can be used for either a sequence of two courses consisting of an introduction to logic circuits Chapters 1 7 followed by logic design Chapters 8 13 or a single accelerated course that uses the early chapters as reference material

Behavioral Synthesis and Component Reuse with VHDL Ahmed Amine Jerraya,1997 Improvement in the quality of integrated circuit designs and a designer's productivity can be achieved by a combination of two factors Using more structured design methodologies for extensive reuse of existing components and subsystems It seems that 70% of new designs correspond to existing components that cannot be reused because of a lack of methodologies and tools Providing higher level design tools allowing to start from a higher level of abstraction After the success and the widespread acceptance of logic and RTL synthesis the next step is behavioral synthesis commonly called architectural or high level synthesis Behavioral Synthesis and Component Reuse with VHDL provides methods and techniques for VHDL based behavioral synthesis and component reuse The goal is to develop VHDL modeling strategies for emerging behavioral synthesis tools Special attention is given to structured and modular design methods allowing hierarchical behavioral specification and design reuse The goal of this book is not to discuss behavioral synthesis in general or to discuss a specific tool but to describe the specific issues related to behavioral synthesis of VHDL description This book targets designers who have to use behavioral synthesis tools or who wish to discover the real possibilities of this emerging technology The book will also be of interest to teachers and students interested to learn or to teach VHDL based behavioral synthesis

VHDL and FPLDs in Digital Systems Design, Prototyping and Customization Zoran Salcic,2012-12-06 This book represents an attempt to treat three aspects of digital systems design prototyping and customization in an integrated manner using two major technologies VHSIC Hardware Description Language VHDL as a modeling and specification tool and Field Programmable Logic Devices FPLDs as an implementation technology They together make a very powerful combination for complex digital systems rapid design and prototyping as the important steps towards manufacturing or in the case of feasible quantities they also provide fast system manufacturing Combining these two technologies makes possible implementation of very complex digital systems at the desk VHDL has become a standard tool to capture features of digital systems in a form of behavioral dataflow or structural models providing a high degree of flexibility When augmented by a good simulator VHDL enables extensive verification of features of the system under design reducing uncertainties at the latter phases of design process As such it becomes an unavoidable modeling tool to model digital systems at various levels of abstraction

Digital System Design with VHDL Mark Zwoliński,2000 Electronic systems based on digital principles are becoming ubiquitous A good design approach to these systems is essential and a top down methodology is favoured Such an approach is vastly simplified by the use of computer modeling to describe the systems VHDL is a formal language which allows a designer to model the behaviours and structure of a digital circuit on a computer before

implementation Digital System Design with VHDL is intended both for students on Digital Design courses and practitioners who would like to integrate digital design and VHDL synthesis in the workplace Its unique approach combines the principles of digital design with a guide to the use of VHDL Synthesis issues are discussed and practical guidelines are provided for improving simulation accuracy and performance Features a practical perspective is obtained by the inclusion of real life examples an emphasis on software engineering practices encourages clear coding and adequate documentation of the process demonstrates the effects of particular coding styles on synthesis and simulation efficiency covers the major VHDL standards includes an appendix with examples in Verilog

Digital Systems Design Using VHDL Lizy Kurian John, Charles Roth, 2017-01-01

RTL Hardware Design Using VHDL Pong P. Chu, 2006-04-20 The skills and guidance needed to master RTL hardware design This book teaches readers how to systematically design efficient portable and scalable Register Transfer Level RTL digital circuits using the VHDL hardware description language and synthesis software Focusing on the module level design which is composed of functional units routing circuit and storage the book illustrates the relationship between the VHDL constructs and the underlying hardware components and shows how to develop codes that faithfully reflect the module level design and can be synthesized into efficient gate level implementation Several unique features distinguish the book Coding style that shows a clear relationship between VHDL constructs and hardware components Conceptual diagrams that illustrate the realization of VHDL codes Emphasis on the code reuse Practical examples that demonstrate and reinforce design concepts procedures and techniques Two chapters on realizing sequential algorithms in hardware Two chapters on scalable and parameterized designs and coding One chapter covering the synchronization and interface between multiple clock domains Although the focus of the book is RTL synthesis it also examines the synthesis task from the perspective of the overall development process Readers learn good design practices and guidelines to ensure that an RTL design can accommodate future simulation verification and testing needs and can be easily incorporated into a larger system or reused Discussion is independent of technology and can be applied to both ASIC and FPGA devices With a balanced presentation of fundamentals and practical examples this is an excellent textbook for upper level undergraduate or graduate courses in advanced digital logic Engineers who need to make effective use of today s synthesis software and FPGA devices should also refer to this book

Digital System Design Using VHDL Chin-Hwa Lee, 1992 This is a new text book introducing VHDL hardware description language top down system design The book emphasizes the difference between regular high level computer language VHDL As soon as VHDL constructs are introduced readers are guided through a progressive series of examples to show the modeling techniques More complex examples are introduced in later chapters to show the top down system design methodology Distinguished features include 89 examples of VHDL programming examples Examples are available on diskette upon request Exercises problems at the end of chapters Answer book available MSI SSI logic circuits modeling Timing modeling accuracy discussion Corresponding behavioral dataflow structural models Models of

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