CHAPTER 5

Chip Seal Materials Selection

The performance of a chip seal is largely dependent on the materials used. Therefore, selecting the appropriate aggregates and asphalt emulsion and deciding whether to apply a fog seal to the surface play a significant role in the success of the project. The following discussion provides guidance regarding these factors.

5.1 Chip Gradation

The gradation of the chip should be one- or two-sized, but the maximum size should be selected based on traffic volume, pavement texture, and the required level of sealing. Generally, larger aggregate provides greater ability to seal because of the higher volume of binder required to hold the chips in place, and depending on traffic volume, provides longer life expectancy. However, larger aggregates increase the chances for vehicle damage, noise, and roughness.

5.2 Modified or Unmodified Emulsion

Modified emulsions usually refer to some sort of elastomeric polymer or rubber added to the emulsion or to the base asphalt binder prior to emulsification. Because modified emulsions should offer greater adhesivity and potentially shorter time required before opening to traffic, they are generally used on higher traffic pavements, where vehicle damage potential is greater and where limited time under traffic control is usually desirable.

5.3 Fog Seal After Chip Seal

A fog seal may be applied to any completed chip seal as a means of providing a high color contrast for paint stripes. There are also some preliminary indications that the fog seal provides some additional waterproofing (Shuler 2007). Care should be taken whenever applying a fog seal since pavement friction could be reduced if the fog seal is applied at too high an application rate, the fog seal emulsion has a high residue content, or the fog seal has not broken sufficiently to support uncontrolled traffic.

5.4 Emulsion-Aggregate Compatibility

There is anecdotal evidence of apparent incompatibility arising from use of anionic or cationic emulsions with siliceous or calcareous aggregates, respectively. This incompatibility manifests itself with a loss of aggregate from the chip seal. This behavior was not observed during the NCHRP Project 14-17 research, in which 20 combinations of aggregates and emulsions were represented. Therefore, unless impractical, anionic emulsions should be pared with positively charged aggregates (i.e., calcareous), and cationic emulsions should be matched with negatively charged aggregates (i.e., siliceous) to avoid possible incompatibility between the materials.

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David G. Peshkin

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Chip Seal Best Practices Douglas D. Gransberg, David M. B. James, National Cooperative Highway Research Program, 2005 TRB's National Cooperative Highway Research Program NCHRP Synthesis 342 Chip Seal Best Practices examines ways to assist in the development and implementation of pavement preservation programs by identifying the benefits of using chip seal as part of a preventive maintenance program and by highlighting advanced chip seal programs in use around the world The report includes approximately 40 best practices in the areas of chip seal design methods contract administration equipment practices construction practices and performance measures According to the report the increased use of chip seals for maintenance can be a successful cost effective way of using preventive maintenance to preserve both low volume and higher volume pavements Kansas Department of Transportation 2014 Chip Seal Manual Dean M. Testa, 2014 A chip seal is a very effective thin surface treatment process used by maintenance managers to preserve existing asphalt pavements The Kansas Department of Transportation KDOT 2014 Chip Seal Manual is a guide that provides guidelines background and general information for the design construction and inspection of chip seals This manual is intended for use by KDOT field engineers laboratory personnel construction inspectors and contractor s estimators supervisors operators and workers to provide procedures for the design of chip seals The information recommendations and best practices provided in this manual may refer to either 1 learning the overall chip seal operation 2 learning about the workings maintenance calibration and proper operation of equipment used in chip sealing or 3 learning chip seal design procedures This manual consists of seven chapters Chapter 1 is the introduction Chapter 2 is about selection of chip seal Chapter 3 discusses the materials for chip seal and the design process Chapter 4 discusses surface preparation prior to chip sealing Chapter 5 describes equipment inspection and calibration Chapter 6 discusses the actual application process Chapter 7 underlines the areas of concern during construction process Guide for a Road Inventory Manual of Instructions, 1967

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