

Compiling Esterel

Dumitru Potop-Butucaru

Stephen A. Edwards

G rard Berry

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Marilyn Wolf



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Compiling Esterel Dumitru Potop-Butucaru, Stephen A. Edwards, Gerard Berry, 2008-11-01 Designed as the definitive reference on the compilation of the Esterel synchronous reactive real time language *Compiling Esterel* covers all aspects of the language The book includes a tutorial a reference manual formal semantics and detailed technical information about the many techniques used to compile it Researchers as well as advanced developers will find this book essential for understanding Esterel at all levels

Compiling Esterel for Distributed Execution Li Hsien Yoong, 2005

High-Performance Embedded Computing

Wayne Wolf, 2010-07-26 Over the past several years embedded systems have emerged as an integral though unseen part of many consumer industrial and military devices The explosive growth of these systems has resulted in embedded computing becoming an increasingly important discipline The need for designers of high performance application specific computing systems has never been greater and many universities and colleges in the US and worldwide are now developing advanced courses to help prepare their students for careers in embedded computing High Performance Embedded Computing Architectures Applications and Methodologies is the first book designed to address the needs of advanced students and industry professionals Focusing on the unique complexities of embedded system design the book provides a detailed look at advanced topics in the field including multiprocessors VLIW and superscalar architectures and power consumption Fundamental challenges in embedded computing are described together with design methodologies and models of computation HPEC provides an in depth and advanced treatment of all the components of embedded systems with discussions of the current developments in the field and numerous examples of real world applications Covers advanced topics in embedded computing including multiprocessors VLIW and superscalar architectures and power consumption Provides in depth coverage of networks reconfigurable systems hardware software co design security and program analysis

Includes examples of many real world embedded computing applications cell phones printers digital video and architectures the Freescale Starcore TI OMAP multiprocessor the TI C5000 and C6000 series and others **High-Performance**

Embedded Computing Marilyn Wolf, 2014-03-17 High Performance Embedded Computing Second Edition combines leading edge research with practical guidance in a variety of embedded computing topics including real time systems computer architecture and low power design Author Marilyn Wolf presents a comprehensive survey of the state of the art and guides you to achieve high levels of performance from the embedded systems that bring these technologies together The book covers CPU design operating systems multiprocessor programs and architectures and much more Embedded computing is a key component of cyber physical systems which combine physical devices with computational resources for control and communication This revised edition adds new content and examples of cyber physical systems throughout the book including design methodologies scheduling and wide area CPS to illustrate the possibilities of these new systems Revised and updated with coverage of recently developed consumer electronics architectures and models of computing Includes new VLIW processors such as the TI Da Vinci and CPU simulation Learn model based verification and middleware for embedded systems Supplemental material includes lecture slides labs and additional resources [Proceedings Of The](#)

[Singaporean-french Ipal Symposium 2009 - Sinfra'09 \(Cd-rom\)](#) Wee Kheng Leow, Daniel Racoceanu, Feng Ding, Hao Li, 2009-02-11 This Singaporean French Symposium aims to strengthen existing research collaboration between French and Singapore scientists and to explore and develop new collaborations in IPAL Image Perception Access Language the Singaporean French international research lab created between the French National Research Center CNRS the National University of Singapore NUS the Institute for Infocomm Research I2 R A STAR and the Joseph Fourier University UJF SinFra 09 welcomes researchers and collaborators under IPAL organizations and those who wish to collaborate with IPAL The symposium proposes Tutorials and Scientific Sessions in relevant Singaporean French common interest fields related to the present and possible future activities in IPAL We seek submissions of high quality original work in the following areas existing IPAL areas like Medical Image Analysis and Access Mobile Information Access and also new areas of collaboration between French and Singaporean scientists such as interactive digital media cognitive vision and audio video surveillance and security real time embedded systems media and sensors networks programming languages language based security and quantum informatics **From Model-Driven Design to Resource Management for Distributed Embedded Systems**

Bernd Kleinjohann, Lisa Kleinjohann, Ricardo J. Machado, Carlos Pereira, P.S. Thiagarajan, 2007-01-29 Embedded computing systems have started to carry out the key control functions in diverse domains such as telecommunications automotive electronics avionics and even complete industrial manufacturing lines Traditionally such embedded control systems have been implemented in a monolithic centralized manner However distributed and parallel solutions have been steadily gaining popularity In a distributed setup the control task is carried out by a number of controllers distributed over the entire system

and interconnected as a network by communication components such as field buses More demanding local control applications require controllers based on parallel architectures or processors with dedicated co processors Distribution and parallelism in embedded system design increase the engineering challenges and demand new development methods and tools From Model Driven Design to Resource Management for Distributed Embedded Systems contains 16 original contributions as well as 12 invited papers by distinguished invited speakers These papers were presented at the Working Conference on Distributed and Parallel Embedded Systems DIPES 2006 which was held in October 2006 in Braga Portugal and sponsored by the International Federation for Information Processing IFIP This volume covers the following very timely topics model driven design test and evolution of embedded systems timing analysis and predictability scheduling allocation communication and resource management in distributed real time systems

Synthesis Techniques and Optimizations for Reconfigurable Systems Ryan Kastner,Adam Kaplan,Majid Sarrafzadeh,2003-10-27 Synthesis Techniques and Optimization for Reconfigurable Systems discusses methods used to model reconfigurable applications at the system level many of which could be incorporated directly into modern compilers The book also discusses a framework for reconfigurable system synthesis which bridges the gap between application level compiler analysis and high level device synthesis The development of this framework discussed in Chapter 5 and the creation of application analysis which further optimize its output discussed in Chapters 7 8 and 9 represent over four years of rigorous investigation within UCLA s Embedded and Reconfigurable Laboratory ERLab and UCSB s Extensible Programmable and Reconfigurable Embedded SystemS ExPRESS Group The research of these systems has not yet matured and we continually strive to develop data and methods which will extend the collective understanding of reconfigurable system synthesis

European Control Conference 1991 ,1991-07-02
 Proceedings of the European Control Conference 1991 July 2 5 1991 Grenoble France

Formal Methods for Industrial Critical Systems Stefania Gnesi,Tiziana Margaria,2012-11-27 Today formal methods are widely recognized as an essential step in the design process of industrial safety critical systems In its more general definition the term formal methods encompasses all notations having a precise mathematical semantics together with their associated analysis methods that allow description and reasoning about the behavior of a system in a formal manner Growing out of more than a decade of award winning collaborative work within the European Research Consortium for Informatics and Mathematics Formal Methods for Industrial Critical Systems A Survey of Applications presents a number of mainstream formal methods currently used for designing industrial critical systems with a focus on model checking The purpose of the book is threefold to reduce the effort required to learn formal methods which has been a major drawback for their industrial dissemination to help designers to adopt the formal methods which are most appropriate for their systems and to offer a panel of state of the art techniques and tools for analyzing critical systems

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