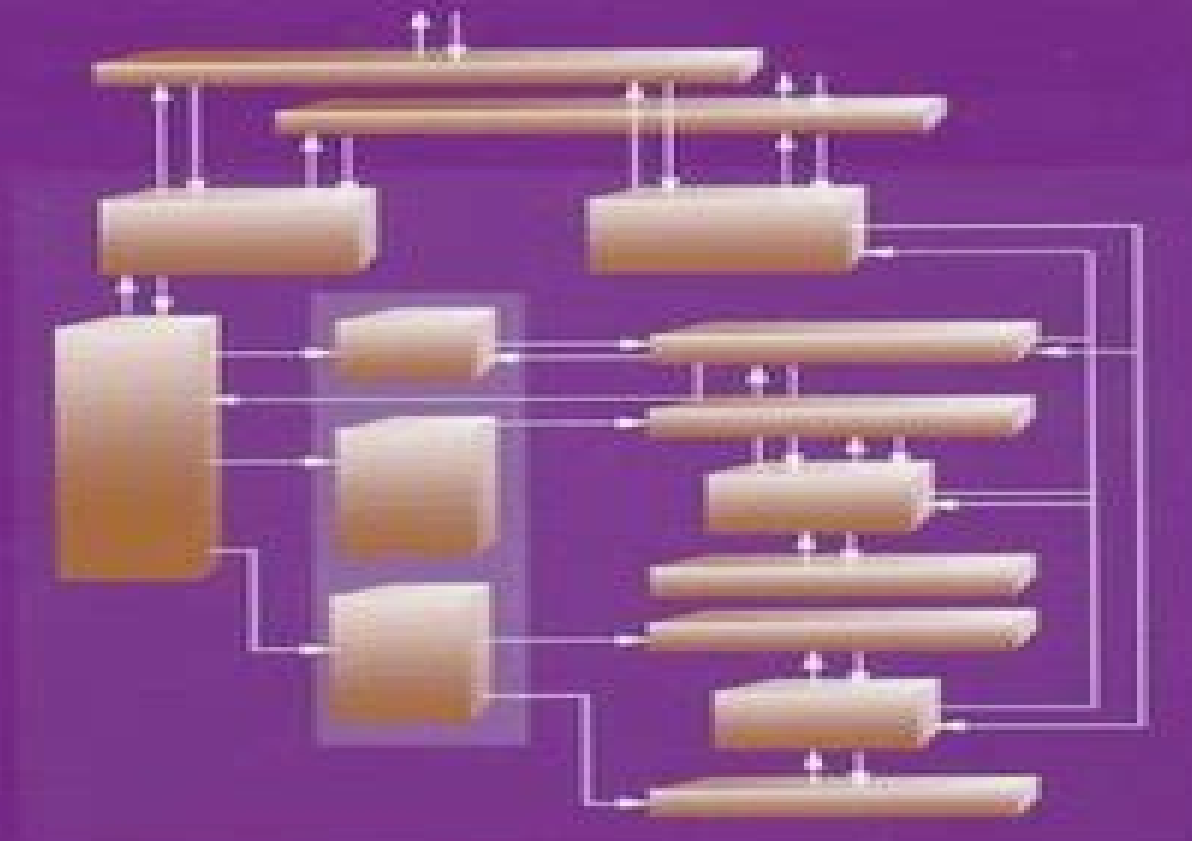


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Computer Architecture and Organization



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Fundamentals of Computer Organization and Architecture Mostafa Abd-El-Barr, Hesham El-Rewini, 2005-02-08 This is the first book in the two volume set offering comprehensive coverage of the field of computer organization and architecture This book provides complete coverage of the subjects pertaining to introductory courses in computer organization and architecture including Instruction set architecture and design Assembly language programming Computer arithmetic Processing unit design Memory system design Input output design and organization Pipelining design techniques Reduced Instruction Set Computers RISCs The authors who share over 15 years of undergraduate and graduate level instruction in computer architecture provide real world applications examples of machines case studies and practical experiences in each chapter

Digital Design and Computer Organisation D. Nasib S. Gill, J.B. Dixit, 2008-12 Digital Design and Computer Organization introduces digital design as it applies to the creation of computer systems It summarizes the tools of logic design and their mathematical basis along with in depth coverage of combinational and sequential circuits The book includes an accompanying CD that includes the majority of circuits highlighted in the text delivering you hands on experience in the simulation and observation of circuit functionality These circuits were designed and tested with a user friendly Electronics Workbench package Multisim Textbook Edition that enables your progression from truth tables onward to more complex designs This volume differs from traditional digital design texts by providing a complete design of an AC based CPU allowing you to apply digital design directly to computer architecture The book makes minimal reference to electrical properties and is vendor independent allowing emphasis on the general design principles

Computer Fundamentals B. Ram, 2000 **COMPUTER ORGANIZATION AND ARCHITECTURE** V. RAJARAMAN, T. RADHAKRISHNAN, 2007-06-01 Designed as an introductory text for the students of computer science computer applications electronics engineering and information technology for their first course on the organization and architecture of computers this accessible student friendly text gives a clear and in depth analysis of the basic principles underlying the subject This self contained text devotes one full chapter to the basics of digital logic While the initial chapters describe in detail about computer organization including CPU design ALU design memory design and I O organization the text also deals with Assembly Language Programming for Pentium using NASM assembler What distinguishes the text is the special attention it pays to Cache and Virtual Memory organization as well as to RISC architecture and the intricacies of pipelining All these discussions are climaxed by an illuminating discussion on parallel

computers which shows how processors are interconnected to create a variety of parallel computers KEY FEATURES Self contained presentation starting with data representation and ending with advanced parallel computer architecture Systematic and logical organization of topics Large number of worked out examples and exercises Contains basics of assembly language programming Each chapter has learning objectives and a detailed summary to help students to quickly revise the material **Computer Architecture and Organization: From 8085 to core2Duo & beyond** Subrata Ghoshal,2011 The book uses microprocessors 8085 and above to explain the various concepts It not only covers the syllabi of most Indian universities but also provides additional information about the latest developments like Intel Core II Duo making it one of the most updated textbook in the market The book has an excellent pedagogy sections like food for thought and quicksand corner make for an interesting read **Computer Architecture And Organization** Ian East,2004-01-14

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Programming Languages and System Architectures Jürg Gutknecht,1994-02-22 Programming languages and system architectures are at the frontiers of two different worlds The conference on which this book is based was an adventure in a land where the two worlds the formal world of algorithms and the physical world of electronic circuits interact The participants explored this land under the guidance of internationally renowned researchers such as Butler W Lampson Susan Graham Jan L A van de Snepscheut and C A R Hoare all of whom gave invited papers The volume includes these papers together with sixteen session papers Subjects of special interest include programing language design and history programming environments programming methods operating systems compiler construction and innovative system

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