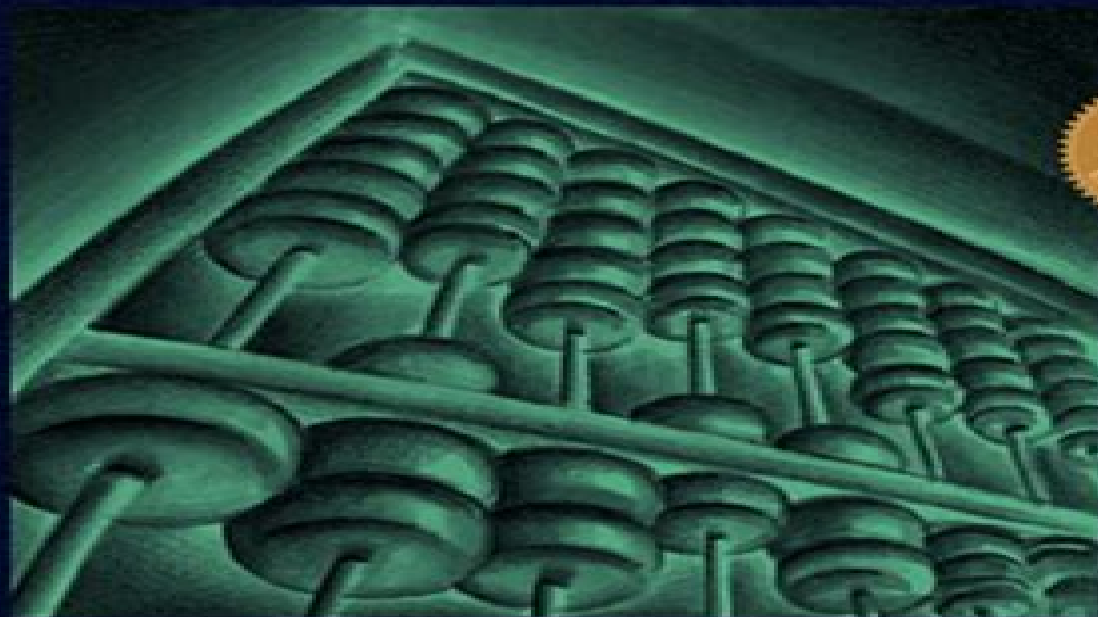




COMPUTER ORGANIZATION AND DESIGN

THE HARDWARE/SOFTWARE INTERFACE



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Computer Organization and Design David A. Patterson, John L. Hennessy, 2004-08-07 This best selling text on computer organization has been thoroughly updated to reflect the newest technologies Examples highlight the latest processor designs benchmarking standards languages and tools As with previous editions a MIPS processor is the core used to present the fundamentals of hardware technologies at work in a computer system The book presents an entire MIPS instruction set instruction by instruction the fundamentals of assembly language computer arithmetic pipelining memory hierarchies and I O A new aspect of the third edition is the explicit connection between program performance and CPU performance The authors show how hardware and software components such as the specific algorithm programming language compiler ISA and processor implementation impact program performance Throughout the book a new feature focusing on program performance describes how to search for bottlenecks and improve performance in various parts of the system The book digs deeper into the hardware software interface presenting a complete view of the function of the programming language and compiler crucial for understanding computer organization A CD provides a toolkit of simulators and compilers along with tutorials for using them For instructor resources click on the grey companion site button found on the right side of this page This new edition represents a major revision New to this edition Entire Text has been updated to reflect new technology 70% new exercises Includes a CD loaded with software projects and exercises to support courses using a number of tools A new interior design presents defined terms in the margin for quick reference A new feature Understanding Program Performance focuses on performance from the programmer s perspective Two sets of exercises and solutions For More Practice and In More Depth are included on the CD Check Yourself questions help students check their understanding of major concepts Computers In the Real World feature illustrates the diversity of uses for information technology More detail below

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computing devices architectures is included An online companion Web site provides advanced content for further study appendices glossary references and recommended reading Features RISC V the first such architecture designed to be used in modern computing environments such as cloud computing mobile devices and other embedded systems Includes relevant examples exercises and material highlighting the emergence of mobile computing and the cloud

Parallel Programming Thomas Rauber, Gudula Rünger, 2023-04-04 This textbook covers the new development in processor architecture and parallel hardware It provides detailed descriptions of parallel programming techniques that are necessary for developing efficient programs for multicore processors as well as for parallel cluster systems and supercomputers The book is structured in three main parts covering all areas of parallel computing the architecture of parallel systems parallel programming models and environments and the implementation of efficient application algorithms The emphasis lies on parallel programming techniques needed for different architectures In particular this third edition includes an extended update of the chapter on computer architecture and performance analysis taking new developments such as the aspect of energy consumption into consideration The description of OpenMP has been extended and now also captures the task concept of OpenMP The chapter on message passing programming has been extended and updated to include new features of MPI such as extended reduction operations and non blocking collective communication operations The chapter on GPU programming also has been updated All other chapters also have been revised carefully The main goal of this book is to present parallel programming techniques that can be used in many situations for many application areas and to enable the reader to develop correct and efficient parallel programs Many example programs and exercises are provided to support this goal and to show how the techniques can be applied to further applications The book can be used as a textbook for students as well as a reference book for professionals The material of the book has been used for courses in parallel programming at different universities for many years

VLSI Design and Test for Systems Dependability Shojiro Asai, 2018-07-20 This book discusses the new roles that the VLSI very large scale integration of semiconductor circuits is taking for the safe secure and dependable design and operation of electronic systems The book consists of three parts Part I as a general introduction to this vital topic describes how electronic systems are designed and tested with particular emphasis on dependability engineering where the simultaneous assessment of the detrimental outcome of failures and cost of their containment is made This section also describes the related research project Dependable VLSI Systems in which the editor and authors of the book were involved for 8 years Part II addresses various threats to the dependability of VLSIs as key systems components including time dependent degradations variations in device characteristics ionizing radiation electromagnetic interference design errors and tampering with discussion of technologies to counter those threats Part III elaborates on the design and test technologies for dependability in such applications as control of robots and vehicles data processing and storage in a cloud environment and heterogeneous wireless telecommunications This book is intended to be used as a reference for engineers who work on the

design and testing of VLSI systems with particular attention to dependability It can be used as a textbook in graduate courses as well Readers interested in dependable systems from social and industrial economic perspectives will also benefit from the discussions in this book **Computer Arithmetic and Validity** Ulrich Kulisch, 2013-04-30 This is the revised and extended second edition of the successful basic book on computer arithmetic It is consistent with the newest recent standard developments in the field The book shows how the arithmetic and mathematical capability of the digital computer can be enhanced in a quite natural way The work is motivated by the desire and the need to improve the accuracy of numerical computing and to control the quality of the computed results validity The accuracy requirements for the elementary floating point operations are extended to the customary product spaces of computations including interval spaces The mathematical properties of these models are extracted into an axiomatic approach which leads to a general theory of computer arithmetic Detailed methods and circuits for the implementation of this advanced computer arithmetic on digital computers are developed in part two of the book Part three then illustrates by a number of sample applications how this extended computer arithmetic can be used to compute highly accurate and mathematically verified results The book can be used as a high level undergraduate textbook but also as reference work for research in computer arithmetic and applied mathematics

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