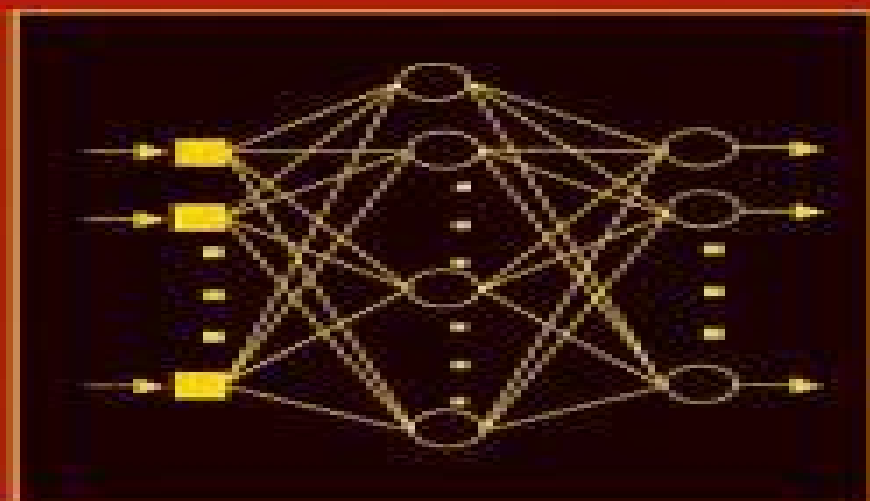


Advances in
COMPUTERS

Volume **96**
Dataflow Processing



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Dataflow Processing Volume 96 Advances In Computers

Veljko Milutinovic, Milos Kotlar



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Exploring the DataFlow Supercomputing Paradigm Veljko Milutinovic, Milos Kotlar, 2019-05-27 This useful text reference describes the implementation of a varied selection of algorithms in the DataFlow paradigm highlighting the exciting potential of DataFlow computing for applications in such areas as image understanding biomedicine physics simulation and business The mapping of additional algorithms onto the DataFlow architecture is also covered in the following Springer titles from the same team DataFlow Supercomputing Essentials Research Development and Education DataFlow Supercomputing Essentials Algorithms Applications and Implementations and Guide to DataFlow Supercomputing Topics and Features introduces a novel method of graph partitioning for large graphs involving the construction of a skeleton graph describes a cloud supported web based integrated development environment that can develop and run programs without DataFlow hardware owned by the user showcases a new approach for the calculation of the extrema of functions in one dimension by implementing the Golden Section Search algorithm reviews algorithms for a DataFlow architecture that uses matrices and vectors as the underlying data structure presents an algorithm for spherical code design based on the variable repulsion force method discusses the implementation of a face recognition application using the DataFlow paradigm proposes a method for region of interest based image segmentation of mammogram images on high performance reconfigurable DataFlow computers surveys a diverse range of DataFlow applications in physics simulations and investigates a DataFlow implementation of a Bitcoin mining algorithm This unique volume will prove a valuable reference for researchers and programmers of DataFlow computing and supercomputing in general Graduate and advanced undergraduate students will also find that the book serves as an ideal supplementary text for courses on Data Mining Microprocessor Systems and VLSI Systems

Advances in Computers, 2017-06-26 *Advances in Computers* Volume 106 is the latest volume in the series which has been published since 1960 This update presents innovations in computer hardware software theory design and applications with new chapters in this volume including sections on A New Course on R D Project Management in Computer Science and Engineering Subjects Taught Rationales Behind and Lessons Learned Advances in Dataflow Systems Adaptation and Evaluation of the Simplex Algorithm for a Data Flow Architecture and Simple Operations in Memory to Reduce Data Movement In addition this series provides contributors with a medium to

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Supercomputing Essentials Veljko Milutinovic,Jakob Salom,Dragan Veljovic,Nenad Korolija,Dejan Markovic,Luka Petrovic,2017-10-30 This informative text reference highlights the potential of DataFlow computing in research requiring high speeds low power requirements and high precision while also benefiting from a reduction in the size of the equipment The cutting edge research and implementation case studies provided in this book will help the reader to develop their practical understanding of the advantages and unique features of this methodology This work serves as a companion title to DataFlow Supercomputing Essentials Algorithms Applications and Implementations which reviews the key algorithms in this area and provides useful examples Topics and features reviews the library of tools applications and source code available to support DataFlow programming discusses the enhancements to DataFlow computing yielded by small hardware changes different compilation techniques debugging and optimizing tools examines when a DataFlow architecture is best applied and for which types of calculation describes how converting applications to a DataFlow representation can result in an acceleration in performance while reducing the power consumption explains how to implement a DataFlow application on Maxeler hardware architecture with links to a video tutorial series available online This enlightening volume will be of great interest to all researchers investigating supercomputing in general and DataFlow computing in particular Advanced undergraduate and graduate students involved in courses on Data Mining Microprocessor Systems and VLSI Systems will also find the book to be a helpful reference **Creativity in Computing and DataFlow SuperComputing** ,2017-01-02

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coverage that is devoted to single themes or subfields of computer science *Advances in Computer Technology and Applications in Japan* Thirwalam K.S. Murthy, Carlos A. Brebbia, 2013-03-13 This book contains chapters written by eminent scientists on the latest development in computer technology and applications in Japan The objective of the book is to provide an awareness of the considerable advances being made by Japanese scientists on the general area of information technology and in the so called Fifth Generation Computer Systems In the first chapter Watanabe of the NEC Corporation describes advanced architecture and technology of supercomputing systems This theme is followed by Nakamura of Tohoku University in the next chapter Another type of supercomputer for vector processing the FACOM VP 2000 Series is then described by Uchida of Fujitsu Ltd in Chapter 3 Expert systems are presented in the next two chapters by Ueno and Oomari of Tokyo Denki University and by Koseki and Goto of the NEC Corporation Important applications in computer graphics are described in Chapter Six by Ishii and Murakami of Fujitsu Laboratories Hayahi from the same Laboratory then discusses neurocomputers in Japan The final chapter by Noguchi of Tohoku University illustrates an important application in communications Advances in Computer Systems Architecture Lynn Choi, Yunheung Paek, Sangyeun Cho, 2007-08-21 The refereed proceedings of the 12th Asia Pacific Computer Systems Architecture Conference are presented in this volume Twenty six full papers are presented together with two keynote and eight invited lectures Collectively they represent some of the most important developments in computer systems architecture The papers emphasize hardware and software techniques for state of the art multi core and multi threaded architectures Embedded Computer Vision Branislav Kisanin, Shuvra S. Bhattacharyya, Sek Chai, 2008-09-26 As a graduate student at Ohio State in the mid 1970s I inherited a unique computer vision laboratory from the doctoral research of previous students They had designed and built an early frame grabber to deliver digitized color video from a very large electronic video camera on a tripod to a mini computer system with a huge disk drive about the size of four washing machines They had also signed a binary image array processor and programming language complete with a user's guide to facilitate designing software for this one of a kind processor The overall system enabled programmable real time image processing at video rate for many operations I had the whole lab to myself I designed software that detected an object in the field of view tracked its movements in real time and displayed a running description of the events in English For example An object has appeared in the upper right corner It is moving down and to the left Now the object is getting closer The object moved out of sight to the left about like that The algorithms were simple relying on a sufficient image intensity difference to separate the object from the background a plain wall From computer vision papers I had read I knew that vision in general imaging conditions is much more sophisticated But it worked it was great fun and I was hooked **Advances in Computer Systems Architecture** Chris Jesshope, 2006-08-31 This book constitutes the refereed proceedings of the 11th Asia Pacific Computer Systems Architecture Conference ACSAC 2006 The book presents 60 revised full papers together with 3 invited lectures addressing such issues as processor and

network design reconfigurable computing and operating systems and low level design issues in both hardware and systems
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