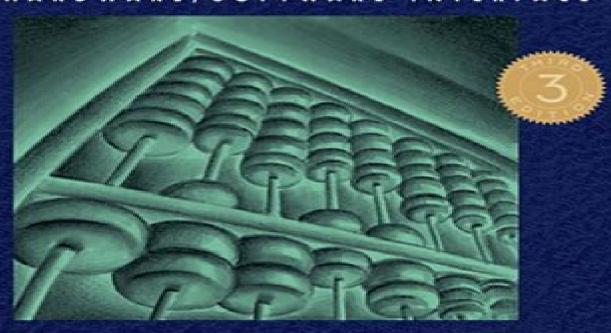


# COMPUTER ORGANIZATION AND DESIGN



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# **Computer Organization Design 3rd Ed Solution Manual**

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HDL constructs and synthesis of basic digital circuits Part II provides an overview of embedded software development with the emphasis on low level I O access and drivers Part III demonstrates the design and development of hardware and software for several complex I O peripherals including PS2 keyboard and mouse a graphic video controller an audio codec and an SD secure digital card Part IV provides three case studies of the integration of hardware accelerators including a custom GCD greatest common divisor circuit a Mandelbrot set fractal circuit and an audio synthesizer based on DDFS direct digital frequency synthesis methodology The book utilizes FPGA devices Nios II soft core processor and development platform from Altera Co which is one of the two main FPGA manufactures Altera has a generous university program that provides free software and discounted prototyping boards for educational institutions details at www altera com university The two main educational prototyping boards are known as DE1 99 and DE2 269 All experiments can be implemented and tested with these boards A board combined with this book becomes a turn key solution for the SoPC design experiments and projects Most HDL and C codes in the book are device independent and can be adapted by other prototyping boards as long as a board has similar I O configuration **Resources in Education** ,1997 Applied Mechanics Reviews, 1986 Catalog of Copyright Entries. Third Series Library of Congress. Copyright Office, 1964 Includes Part 1 Number 1 Books and Pamphlets Including Serials and Contributions to Periodicals January June **Data Processing Digest** ,1984 Books in **Print** ,1991 Scientific and Technical Books in Print ,1972

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