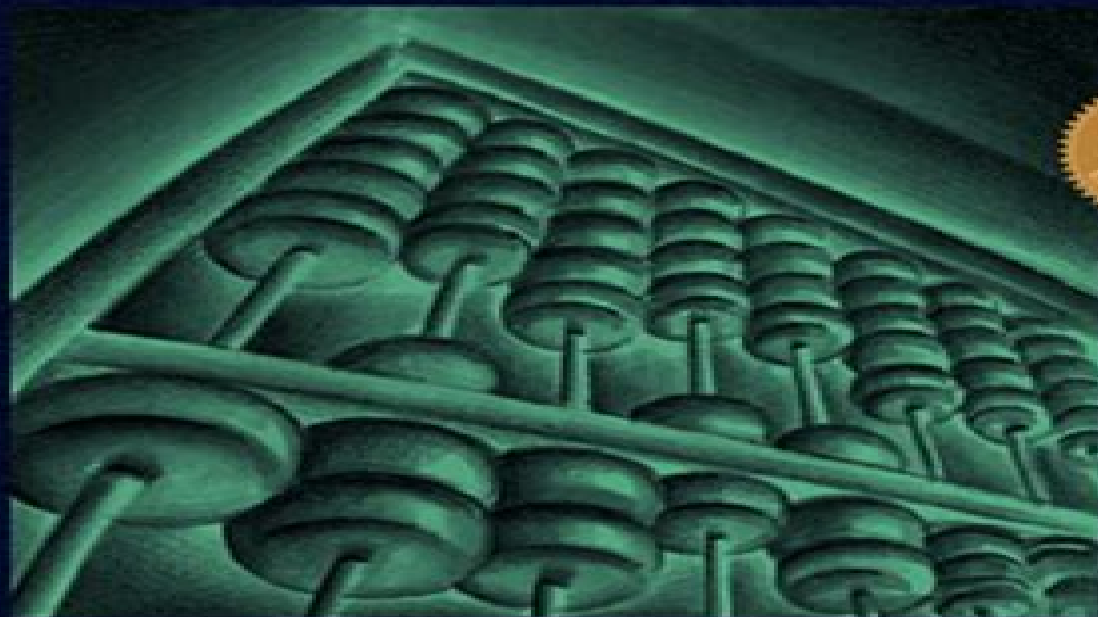




COMPUTER ORGANIZATION AND DESIGN

THE HARDWARE/SOFTWARE INTERFACE



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Thomas Rauber, Gudula Rünger



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Embedded SoPC Design with Nios II Processor and VHDL Examples Pong P. Chu, 2011-08-29 The book is divided into four major parts Part I covers HDL constructs and synthesis of basic digital circuits Part II provides an overview of embedded software development with the emphasis on low level I O access and drivers Part III demonstrates the design and development of hardware and software for several complex I O peripherals including PS2 keyboard and mouse a graphic video controller an audio codec and an SD secure digital card Part IV provides three case studies of the integration of hardware accelerators including a custom GCD greatest common divisor circuit a Mandelbrot set fractal circuit and an audio synthesizer based on DDFS direct digital frequency synthesis methodology The book utilizes FPGA devices Nios II soft

core processor and development platform from Altera Co which is one of the two main FPGA manufactures Altera has a generous university program that provides free software and discounted prototyping boards for educational institutions details at www.altera.com/university The two main educational prototyping boards are known as DE1 99 and DE2 269 All experiments can be implemented and tested with these boards A board combined with this book becomes a turn key solution for the SoPC design experiments and projects Most HDL and C codes in the book are device independent and can be adapted by other prototyping boards as long as a board has similar I O configuration

Parallel Programming Thomas Rauber,Gudula Rünger,2023-04-04 This textbook covers the new development in processor architecture and parallel hardware It provides detailed descriptions of parallel programming techniques that are necessary for developing efficient programs for multicore processors as well as for parallel cluster systems and supercomputers The book is structured in three main parts covering all areas of parallel computing the architecture of parallel systems parallel programming models and environments and the implementation of efficient application algorithms The emphasis lies on parallel programming techniques needed for different architectures In particular this third edition includes an extended update of the chapter on computer architecture and performance analysis taking new developments such as the aspect of energy consumption into consideration The description of OpenMP has been extended and now also captures the task concept of OpenMP The chapter on message passing programming has been extended and updated to include new features of MPI such as extended reduction operations and non blocking collective communication operations The chapter on GPU programming also has been updated All other chapters also have been revised carefully The main goal of this book is to present parallel programming techniques that can be used in many situations for many application areas and to enable the reader to develop correct and efficient parallel programs Many example programs and exercises are provided to support this goal and to show how the techniques can be applied to further applications The book can be used as a textbook for students as well as a reference book for professionals The material of the book has been used for courses in parallel programming at different universities for many years

Handbook of Data Structures and Applications Dinesh P. Mehta,Sartaj Sahni,2018-02-21 The Handbook of Data Structures and Applications was first published over a decade ago This second edition aims to update the first by focusing on areas of research in data structures that have seen significant progress While the discipline of data structures has not matured as rapidly as other areas of computer science the book aims to update those areas that have seen advances Retaining the seven part structure of the first edition the handbook begins with a review of introductory material followed by a discussion of well known classes of data structures Priority Queues Dictionary Structures and Multidimensional structures The editors next analyze miscellaneous data structures which are well known structures that elude easy classification The book then addresses mechanisms and tools that were developed to facilitate the use of data structures in real programs It concludes with an examination of the applications of data structures Four new chapters have been added on Bloom Filters

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web 1 use table 1 to determine the reaction order for hcl 2 use table 2 to determine the reaction order for $\text{na}_2\text{s}_2\text{o}_3$ remember you want to see what happens to the reaction

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web title reaction order and rate laws formal lab report date 02 11 abstract the purpose of this lab was to determine the reaction rate of sodium thiosulfate and hydrochloric acid