



The Essentials of Computer Organization and Architecture

Linda Null and Julia Lobur
Jones and Bartlett Publishers, 2003

Chapter 5 Instructor's Manual

Chapter Objectives

Chapter 5, A Closer Look at Instruction Set Architectures, provides a closer look at instruction set architectures, including instruction formats, instruction types, endianness, and addressing modes. Instruction-level pipelining is introduced as well. Real-world ISAs (including Intel, MIPS, and Java) are discussed to reinforce the concepts presented in the chapter.

This chapter should be covered after Chapter 4.

Lectures should focus on the following points:

- **Instruction formats.** Important issues here include instruction length, little versus big endian, register usage (and the use of stacks), and expanding opcodes.
- **Addressing.** Although addressing is an instruction design issue, there are many aspects to consider, the most important of which is addressing modes.
- **Instruction-level pipelining.** The fetch-decode-execute cycle can be overlapped, resulting in faster execution time. Resource conflicts, conditional branching, and data dependencies can slow this process down.
- **Real-world examples of ISAs.** Intel, MIPS, and the Java virtual machine are all presented to reinforce the concepts of the chapter.

Required Lecture Time

The important concepts in Chapter 5 can typically be covered in 3 lecture hours. However, if a teacher wants the students to have a mastery of all topics in Chapter 5, 8 lecture hours are more reasonable. If lecture time is limited, we suggest that the focus be on instruction formats (including big versus little endian, use of registers, and instruction length) and addressing (with considerable attention paid to the various modes).

Computer Organization And Architecture Instructor Manual

William Stallings



Computer Organization And Architecture Instructor Manual:

Instructor's Manual to Accompany Computer Architecture and Organization John P. Hayes, John Patrick Hayes, 1988-01-01 **The Essentials of Computer Organization and Architecture** Linda Null, Julia Lobur, 2006
Computer Architecture Software Engineering **Computer organization and architecture : instructor's manual**
William Stallings, 2000 *The Essentials of Computer Organization and Architecture* Linda Null, Julia Lobur, 2010-12-15
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 Hind, 2017-03-14 This book describes how a computer works and explains how the various hardware components are
 organized and interconnected to provide a platform upon which programs can be executed It takes a simple step by step
 approach suitable for first year undergraduates coming to the subject for the first time The second edition of this book has
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 design computer organization computer hardware and computer system architecture This edition incorporates updates to
 reflect contemporary organizations and devices including graphics processing units GPUs quantum computing and the latest
 supercomputer systems It also includes a description of the two popular Instruction Set Architectures ARM and RISC V The

book is suitable for a one or two semester undergraduate or beginning graduate course in computer science and computer engineering its previous editions have been adopted by 120 universities around the world The book covers the topics suggested by the recent IEEE ACM curriculum for computer architecture and organization

Reviewing **Computer Organization And Architecture Instructor Manual**: Unlocking the Spellbinding Force of Linguistics

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