

Exercise 2: Connecting the Digital Logic Circuits

EXERCISE OBJECTIVE

When you have completed this exercise, you will be able to connect digital logic circuits and observe the inputs and outputs by using the DIGITAL LOGIC FUNDAMENTALS circuit board. You will verify your results with a multimeter and an oscilloscope.

EXERCISE DISCUSSION

Two basic logic gates are demonstrated by each of the following circuit blocks:

- AND/NAND circuit block
- OR/NOR circuit block
- XOR/XNOR circuit block
- OPEN COLLECTOR circuit block
- TTL/CMOS COMPARISON circuit block

One logic circuit is demonstrated by each of the following circuit blocks:

- SET/RESET FLIP-FLOP circuit block
- D-TYPE FLIP-FLOP circuit block
- JK FLIP-FLOP circuit block
- TRI-STATE OUTPUT circuit block

The DATA BUS CONTROL circuit block combines three logic functions to demonstrate data transfer. Every circuit block has a title. This is the AND/NAND circuit block.



Digital Logic Circuits Lab Manual

Asadullah Khan



Digital Logic Circuits Lab Manual:

Digital Circuit Design Laboratory Manual, 4th edition (Global) Akhan Almagambetov, J. Matt Pavlina, Yelena Mukhortova, *Digital Electronics and Microprocessors Lab Manual* Mr. Rohit Manglik, 2024-04-06 EduGorilla Publication is a trusted name in the education sector committed to empowering learners with high quality study materials and resources Specializing in competitive exams and academic support EduGorilla provides comprehensive and well structured content tailored to meet the needs of students across various streams and levels Digital Logic Design, Lab Manual, Volume-I Asadullah Khan, 2017-03-24 **Lab Manual** Gregory L. Moss, 1991 Introduction to Digital Logic Textbook Equity, 2014-09-10 This textbook released under a Creative Commons Share Alike CC BY SA license is presented in its original format with the academic content unchanged It was authored by James Feher and reviewed by colleagues and provided by the University of Georgia's Global Textbook Project This lab manual provides an introduction to digital logic starting with simple gates and building up to state machines Students should have a solid understanding of algebra as well as a rudimentary understanding of basic electricity including voltage current resistance capacitance inductance and how they relate to direct current circuits *Introduction to Logic Circuits & Logic Design with Verilog* Brock J. LaMeres, 2017-04-17 This textbook for courses in Digital Systems Design introduces students to the fundamental hardware used in modern computers Coverage includes both the classical approach to digital system design i.e pen and paper in addition to the modern hardware description language HDL design approach computer based Using this textbook enables readers to design digital systems using the modern HDL approach but they have a broad foundation of knowledge of the underlying hardware and theory of their designs This book is designed to match the way the material is actually taught in the classroom Topics are presented in a manner which builds foundational knowledge before moving onto advanced topics The author has designed the presentation with learning Goals and assessment at its core Each section addresses a specific learning outcome that the student should be able to do after its completion The concept checks and exercise problems provide a rich set of assessment tools to measure student performance on each outcome *Digital Circuits Laboratory Manual* Farzin Asadi, 2023-08-30 Digital systems are an important part of modern life This book introduces the basic building blocks of digital systems and how these blocks can be used to design a digital system It can be used as a laboratory manual for courses such as Digital Logic and Digital Electronics All of the experiments in this book can be done in a simulation environment like Proteus or NI MultiSim or on the breadboard in a real laboratory environment **Digital Logic Design** John F. Passafiume, Michael Douglas, 1985 **Introduction to Logic Circuits & Logic Design with VHDL** Brock J. LaMeres, 2023-10-24 This textbook introduces readers to the fundamental hardware used in modern computers The only pre requisite is algebra so it can be taken by college freshman or sophomore students or even used in Advanced Placement courses in high school This book presents both the classical approach to digital system design i.e pen and paper in addition to the modern hardware

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Digital Logic Simulation with CPLD Programming Steve Waterman, 2003 This lab manual written around software and hardware developments of the past ten years focuses on the fundamentals of digital electronics and use of Max Plus II software by Altera Corporation Lab sequences start with digital gates and logic control circuits progress to MSI devices latches and flip flops and cover clock dependent circuits and the LPM_MACRO functions available in the software For individuals preparing to be computer technicians

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