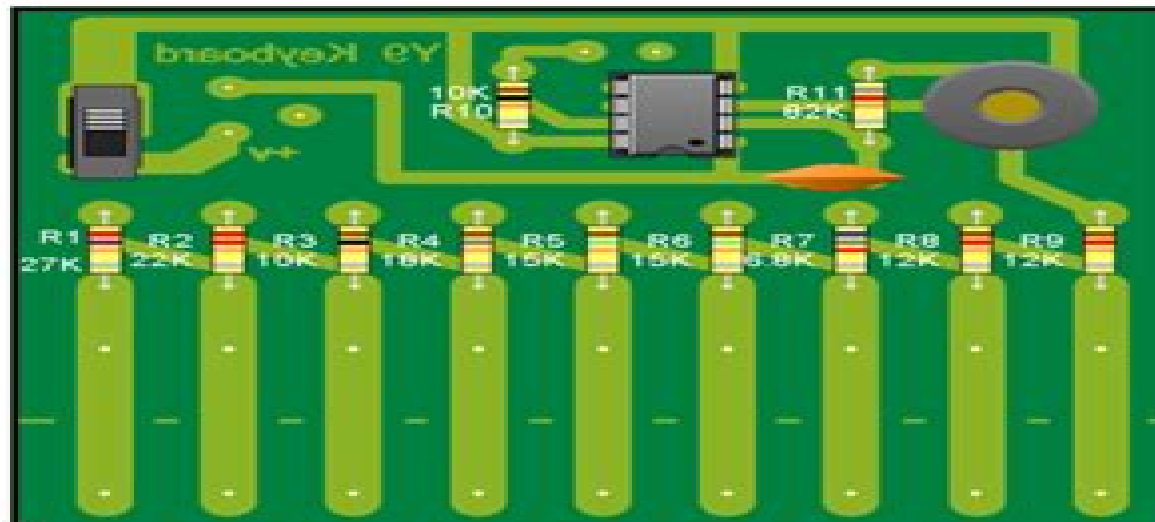




ELECTRICAL CIRCUITS I

Laboratory Manual



Circuits Theory And Networks Lab Manual

Paul Tobin



Circuits Theory And Networks Lab Manual:

PSpice for Circuit Theory and Electronic Devices Paul Tobin, 2007 PSpice for Circuit Theory and Electronic Devices is one of a series of five PSpice books and introduces the latest Cadence Orcad PSpice version 10.5 by simulating a range of DC and AC exercises. It is aimed primarily at those wishing to get up to speed with this version but will be of use to high school students, undergraduate students and of course lecturers. Circuit theorems are applied to a range of circuits and the calculations by hand after analysis are then compared to the simulated results. The Laplace transform and the s plane are used to analyze CR and LR circuits where transient signals are involved. Here the Probe output graphs demonstrate what a great learning tool PSpice is by providing the reader with a visual verification of any theoretical calculations. Series and parallel tuned resonant circuits are investigated where the difficult concepts of dynamic impedance and selectivity are best understood by sweeping different circuit parameters through a range of values. Obtaining semiconductor device characteristics as a laboratory exercise has fallen out of favour of late but nevertheless is still a useful exercise for understanding or modelling semiconductor devices. Inverting and non-inverting operational amplifiers characteristics such as gain, bandwidth are investigated and we will see the dependency of bandwidth on the gain using the performance analysis facility. Power amplifiers are examined where PSpice Probe demonstrates very nicely the problems of cross-over distortion and other problems associated with power transistors. We examine power supplies and the problems of regulation, ground bounce and power factor correction. Lastly we look at MOSFET device characteristics and show how these devices are used to form basic CMOS logic gates such as NAND and NOR gates.

Laboratory Manual for Introductory Electronics Experiments L. K. Maheshwari, M. M. S. Anand, 1979 **EEE.**, 1969 **Trade and Industrial Education**, 1972 **U.S. Government Research & Development Reports**, 1970 **Scientific and Technical Aerospace Reports**, 1995

Subject Guide to Books in Print, 1984 *MOSFET Models for VLSI Circuit Simulation* Narain D. Arora, 2012-12-06

Metal Oxide Semiconductor MOS transistors are the basic building block of MOS integrated circuits. IC Very Large Scale Integrated VLSI circuits using MOS technology have emerged as the dominant technology in the semiconductor industry. Over the past decade the complexity of MOS ICs has increased at an astonishing rate. This is realized mainly through the reduction of MOS transistor dimensions in addition to the improvements in processing. Today VLSI circuits with over 3 million transistors on a chip with effective or electrical channel lengths of 0.5 microns are in volume production. Designing such complex chips is virtually impossible without simulation tools which help to predict circuit behavior before actual circuits are fabricated. However the utility of simulators as a tool for the design and analysis of circuits depends on the adequacy of the device models used in the simulator. This problem is further aggravated by the technology trend towards smaller and smaller device dimensions which increases the complexity of the models. There is extensive literature available on modeling these short channel devices. However there is a lot of confusion too. Often it is not clear what model to use and

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