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Computer Architecture Gérard Blanchet, Bertrand Dupouy, 2013-01-24 This book lays out the concepts necessary to understand how a computer works For reasons of clarity the authors have deliberately chosen examples that apply to machines from all eras without having to water

down the contents of the book This choice helps to show how techniques concepts and performances have evolved since the first computers The book is divided into five parts The first four which are of increasing difficulty are the core of the book Elements of a Basic Architecture Programming Model and Operation Memory Hierarchy Parallelism and Performance Enhancement The final part provides hints and solutions to the exercises in the book as well as appendices The reader may approach each part independently based on their prior knowledge and goals *Readings in Computer Architecture* Mark D. Hill, Gurindar S. Sohi, 2000 Offering a carefully reviewed selection of over 50 papers illustrating the breadth and depth of computer architecture this text includes insightful introductions to guide readers through the primary sources

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Advances in Computer Systems Architecture Amos Omondi, 2003-09-16 This book constitutes the refereed proceedings of the 8th Asia Pacific Computer Systems Architecture Conference ACSAC 2003 held in Aizu Wakamatsu Japan in September 2003 The 23 revised full papers presented together with 8 invited papers were carefully reviewed and selected from 30 submissions The papers are organized in topical sections on processor architectures and innovative microarchitectures parallel computer architectures and computation models reconfigurable architectures computer arithmetic cache and memory architectures and interconnection networks and network interfaces

Ubiquitous Communications and Network Computing Navin Kumar, M. Vinodhini, Ranga Rao Venkatesha Prasad, 2021-07-05 This book constitutes the refereed proceedings of the 4th International Conference on Ubiquitous Communications and Network Computing UBICNET 2021 held in March 2021 Due to COVID 19 pandemic the conference was held virtually The 17 full papers were selected from 59 submissions and are basically arranged in different sessions on 5G networks millimeter wave communication systems and emerging applications quantum communication IoT and emerging applications data analytics

and cloud computing artificial neural network machine learning and emerging applications *VLSI Design and Test for Systems Dependability* Shojiro Asai, 2018-07-20 This book discusses the new roles that the VLSI very large scale integration of semiconductor circuits is taking for the safe secure and dependable design and operation of electronic systems The book consists of three parts Part I as a general introduction to this vital topic describes how electronic systems are designed and tested with particular emphasis on dependability engineering where the simultaneous assessment of the detrimental outcome of failures and cost of their containment is made This section also describes the related research project Dependable VLSI Systems in which the editor and authors of the book were involved for 8 years Part II addresses various threats to the dependability of VLSIs as key systems components including time dependent degradations variations in device characteristics ionizing radiation electromagnetic interference design errors and tampering with discussion of technologies to counter those threats Part III elaborates on the design and test technologies for dependability in such applications as control of robots and vehicles data processing and storage in a cloud environment and heterogeneous wireless telecommunications This book is intended to be used as a reference for engineers who work on the design and testing of VLSI systems with particular attention to dependability It can be used as a textbook in graduate courses as well Readers interested in dependable systems from social and industrial economic perspectives will also benefit from the discussions in this book

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