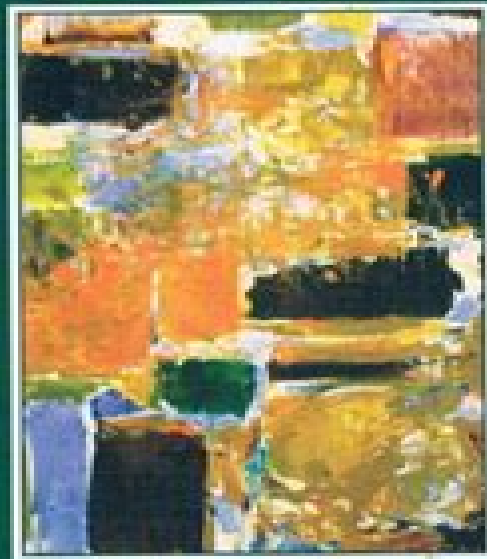


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SECOND EDITION



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Integrated Circuits Jan M. Rabaey, 2003 Contents: p. p. Chapter 1 Introduction p. p. Chapter 2 The Manufacturing Process p. p. Chapter 3 The Devices p. p. Chapter 4 The Wire p. p. Chapter 5 The CMOS Inverter p. p. Chapter 6 Designing Combinational Logic Gates in CMOS

Digital Integrated Circuits Evgeni Perelroyzen, 2018-10-03 A current trend in digital design, the integration of the MATLAB components Simulink and Stateflow for model building, simulations, system testing, and fault detection, allows for better control over the design flow process and ultimately for better system results. *Digital Integrated Circuits Design for Test Using Simulink and Stateflow* illustrates the construction of Simulink models for digital project test benches in certain design-for-test fields. The first two chapters of the book describe the major tools used for design-for-test. The author explains the process of Simulink model building, presents the main library blocks of Simulink, and examines the development of finite state machine modeling using Stateflow diagrams. Subsequent chapters provide examples of Simulink modeling and simulation for the latest design-for-test fields, including combinational and sequential circuits, controllability, and observability, deterministic algorithms, digital circuit dynamics, timing verification, built-in self-test (BIST) architecture, scan cell operations, and functional and diagnostic testing. The book also discusses automatic test pattern generation (ATPG), process, the logical determinant theory, and joint test action group (JTAG) interface models. *Digital Integrated Circuits* explores the possibilities of MATLAB's tools in the development of application-specific integrated circuit (ASIC) design systems. The

book shows how to incorporate Simulink and Stateflow into the process of modern digital design

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Space Microelectronics Volume 2: Integrated Circuit Design for Space Applications Anatoly Belous, Vitali Saladukha, ,Siarhei Shvedau, 2017-07-31 This invaluable second volume of a two volume set is filled with details about the integrated circuit design for space applications Various considerations for the selection and application of electronic components for designing spacecraft are discussed The basic constructions of submicron transistors and schottky diodes during the technological process of production are explored This book provides details on the energy consumption minimization methods for microelectronic devices Specific topics include Features and physical mechanisms of the effect of space radiation on all the main classes of microcircuits including peculiarities of radiation impact on submicron integrated circuits Special design technology and schematic methods of increasing the resistance to various types of space radiation Recommendations for choosing research equipment and methods for irradiating various samples Microcircuit designers on the composition of test elements for the study of the effect of radiation Microprocessors circuit boards logic microcircuits digital analog digital analog microcircuits manufactured in various technologies bipolar CMOS BiCMOS SOI Problems involved with designing high speed microelectronic devices and systems based on SOS and SOI structures System on chip and system in package and methods for rejection of silicon microcircuits with hidden defects during mass production

Embedded SoPC Design with Nios II Processor and Verilog Examples Pong P. Chu, 2012-05-14 Explores the unique hardware programmability of FPGA based embedded

systems using a learn by doing approach to introduce the concepts and techniques for embedded SoPC design with Verilog. An SoPC system on a programmable chip integrates a processor, memory modules, I/O peripherals, and custom hardware accelerators into a single FPGA (field programmable gate array) device. In addition to the customized software, customized hardware can be developed and incorporated into the embedded system as well, allowing us to configure the soft-core processor, create tailored I/O interfaces, and develop specialized hardware accelerators for computation-intensive tasks. Utilizing an Altera FPGA prototyping board and its Nios II soft-core processor, *Embedded SoPC Design with Nios II Processor and Verilog Examples* takes a learn by doing approach to illustrate the hardware and software design and development process by including realistic projects that can be implemented and tested on the board. Emphasizing hardware design and integration throughout, the book is divided into four major parts. Part I covers HDL and synthesis of custom hardware. Part II introduces the Nios II processor and provides an overview of embedded software development. Part III demonstrates the design and development of hardware and software of several complex I/O peripherals, including a PS2 keyboard and mouse, a graphic video controller, an audio codec, and an SD (secure digital) card. Part IV provides several case studies of the integration of hardware accelerators, including a custom GCD (greatest common divisor) circuit, a Mandelbrot set fractal circuit, and an audio synthesizer based on DDFS (direct digital frequency synthesis) methodology. While designing and developing an embedded SoPC can be rewarding, the learning can be a long and winding journey. This book shows the trail ahead and guides readers through the initial steps to exploit the full potential of this emerging methodology.

Digital Integrated Circuits Jan M. Rabaey, 2003 *Introduction to VLSI Systems* Ming-Bo Lin, 2011-11-28

With the advance of semiconductors and ubiquitous computing, the use of system on a chip (SoC) has become an essential technique to reduce product cost. With this progress and continuous reduction of feature sizes and the development of very large scale integration (VLSI) circuits, addressing the harder problems requires fundamental understanding.

FPGA Prototyping by SystemVerilog Examples Pong P. Chu, 2018-05-04

A hands-on introduction to FPGA prototyping and SoC design. This is the successor edition of the popular *FPGA Prototyping by Verilog Examples* text. It follows the same learning by doing approach to teach the fundamentals and practices of HDL synthesis and FPGA prototyping. The new edition uses a coherent series of examples to demonstrate the process to develop sophisticated digital circuits and IP (intellectual property) cores, integrate them into an SoC system on a chip, framework, realize the system on an FPGA prototyping board, and verify the hardware and software operation. The examples start with simple gate-level circuits, progress gradually through the RTL (register transfer level) modules, and lead to a functional embedded system with custom I/O peripherals and hardware accelerators. Although it is an introductory text, the examples are developed in a rigorous manner, and the derivations follow the strict design guidelines and coding practices used for large complex digital systems. The book is completely updated and uses the SystemVerilog language, which absorbs the Verilog language. It presents the hardware design in the SoC context and introduces the hardware

software co design concept Instead of treating examples as isolated entities the book integrates them into a single coherent SoC platform that allows readers to explore both hardware and software programmability and develop complex and interesting embedded system projects The new edition Adds four general purpose IP cores which are multi channel PWM pulse width modulation controller I2C controller SPI controller and XADC Xilinx analog to digital converter controller Introduces a music synthesizer constructed with a DDFS direct digital frequency synthesis module and an ADSR attack decay sustain release envelope generator Expands the original video controller into a complete stream based video subsystem that incorporates a video synchronization circuit a test pattern generator an OSD on screen display controller a sprite generator and a frame buffer Provides a detailed discussion on blocking and nonblocking statements and coding styles Describes basic concepts of software hardware co design with Xilinx MicroBlaze MCS soft core processor Provides an overview of bus interconnect and interface circuit Presents basic embedded system software development Suggests additional modules and peripherals for interesting and challenging projects FPGA Prototyping by SystemVerilog Examples makes a natural companion text for introductory and advanced digital design courses and embedded system courses It also serves as an ideal self teaching guide for practicing engineers who wish to learn more about this emerging area of interest

Digitally Assisted Pipeline ADCs Boris Murmann, Bernhard E. Boser, 2007-05-08 Digitally Assisted Pipeline ADCs Theory and Implementation explores the opportunity to reduce ADC power dissipation by leveraging digital signal processing capabilities in fine line integrated circuit technology The described digitally assisted pipelined ADC uses a statistics based system identification technique as an enabling element to replace precision residue amplifiers with simple open loop gain stages The digital compensation of analog circuit distortion eliminates one key factor in the classical noise speed linearity constraint loop and thereby enables a significant power reduction Digitally Assisted Pipeline ADCs Theory and Implementation describes in detail the implementation and measurement results of a 12 bit 75 MSample sec proof of concept prototype The Experimental converter achieves power savings greater than 60% over conventional implementations Digitally Assisted Pipeline ADCs Theory and Implementation will be of interest to researchers and professionals interested in advances of state of the art in A D conversion techniques

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