INTEGRATED CIRCUITS AND SYSTEMS

Harry D. Foster
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Creating Assertion-Based IP

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Creating Assertion Based Ip Integrated Circuits And Systems

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Creating Assertion Based Ip Integrated Circuits And Systems:

Creating Assertion-Based IP Harry D. Foster,Adam C. Krolnik,2007-11-26 This book presents formal testplanning guidelines with examples focused on creating assertion based verification IP It demonstrates a systematic process for formal specification and formal testplanning and also demonstrates effective use of assertions languages beyond the traditional language construct discussions Note that there many books published on assertion languages such as SystemVerilog assertions and PSL Yet none of them discuss the important process of testplanning and using these languages to create verification IP This is the first book published on this subject **Creating Assertion-Based IP** Harry D. Foster,Adam C. Krolnik,2008-11-01 This book presents formal testplanning guidelines with examples focused on creating assertion based verification IP It demonstrates a systematic process for formal specification and formal testplanning and also demonstrates effective use of assertions languages beyond the traditional language construct discussions Note that there many books published on assertion languages such as SystemVerilog assertions and PSL Yet none of them discuss the important process of testplanning and using these languages to create verification IP This is the first book published on this subject *Wafer Level 3-D ICs Process Technology* Chuan Seng Tan,Ronald J. Gutmann,L. Rafael Reif,2009-06-29 Three dimensional 3D integration is clearly the simplest answer to most of the semiconductor industry s vexing problems heterogeneous integration and red tions of power form factor delay and even cost Conceptually the power latency and form factor of a system with a xed number of transistors all scale roughly linearly with the diameter of the smallest sphere enclosing frequently interacting devices This clearly provides the fundamental motivation behind 3D technologies which vertically stack several strata of device and interconnect layers with high vertical interconnectivity In addition the ability to vertically stack strata with vergent and even incompatible process ows provides for low cost and low parasitic integration of diverse technologies such as sensors energy scavengers nonvolatile memory dense memory fast memory processors and RF layers These capabilities coupled with today s trends of increasing levels of integrated functionality lower power smaller form factor increasingly divergent process ows and functional diversi cation would seem to make 3D technologies a natural choice for most of the semiconductor industry Since the concept of vertical integration of different strata has been around for over 20 years why aren t vertically stacked strata endemic to the semiconductor industry The simple answer to this question is that in the past the 3D advantages while interesting were not necessary due to the tremendous opportunities offered by geometric scaling In addition even when the global interconnect problem of high performance single core processors seemed insurmountable without inno tions such as 3D alternative architectural solutions such as multicores could eff tivelydelaybutnoteliminatetheneedfor3D **Adaptive Techniques for Dynamic Processor Optimization** Alice Wang,Samuel Naffziger,2008-07-23 The integrated circuit has evolved tremendously in recent years as Moore s Law has enabled exponentially more devices and functionality to be packed onto a single piece of silicon In some ways however these

highly integrated circuits of which microprocessors are the flagship example have become victims of their own success Despite dramatic reductions in the switching energy of the transistors these reductions have kept pace neither with the increased integration levels nor with the higher switching frequencies In addition the atomic dimensions being utilized by these highly integrated processors have given rise to much higher levels of random and systematic variation which undercut the gains from process scaling that would otherwise be realized So these factors the increasing impact of variation and the struggle to control power consumption have given rise to a tremendous amount of innovation in the area of adaptive techniques for dynamic processor optimization The fundamental premise behind adaptive processor design is the recognition that variations in manufacturing and environment cause a statically configured operating point to be far too inefficient Inefficient designs waste power and performance and will quickly be surpassed by more adaptive designs just as it happens in the biological realm Organisms must adapt to survive and a similar trend is seen with processors those that are enabled to adapt to their environment will be far more competitive mm-Wave Silicon Technology Ali M. Niknejad,Hossein Hashemi,2008-01-03 This book compiles and presents the research results from the past five years in mm wave Silicon circuits This area has received a great deal of interest from the research community including several university and research groups The book covers device modeling circuit building blocks phased array systems and antennas and packaging It focuses on the techniques that uniquely take advantage of the scale and integration offered by silicon based technologies

Organic Field Effect Transistors Ioannis Kymissis,2008-12-25 Organic Field Effect Transistors presents the state of the art in organic field effect transistors OFETs with a particular focus on the materials and techniques useful for making integrated circuits The monograph begins with some general background on organic semiconductors discusses the types of organic semiconductor materials suitable for making field effect transistors the fabrication processes used to make integrated Circuits and appropriate methods for measurement and modeling Organic Field Effect Transistors is written as a basic introduction to the subject for practitioners It will also be of interest to researchers looking for references and techniques that are not part of their subject area or routine A synthetic organic chemist for example who is interested in making OFETs may use the book more as a device design and characterization reference A thin film processing electrical engineer on the other hand may be interested in the book to learn about what types of electron carrying organic semiconductors may be worth trying and learning more about organic semiconductor physics **Embedded Memories for Nano-Scale VLSIs** Kevin Zhang,2009-04-21 Kevin Zhang Advancement of semiconductor technology has driven the rapid growth of very large scale integrated VLSI systems for increasingly broad applications incl ing high end and mobile computing consumer electronics such as 3D gaming multi function or smart phone and various set top players and ubiquitous sensor and medical devices To meet the increasing demand for higher performance and lower power consumption in many different system applications it is often required to have a large amount of on die or embedded memory to support the need

of data bandwidth in a system The varieties of embedded memory in a given system have alsobecome increasingly more complex ranging fromstatictodynamic and volatile to nonvolatile Among embedded memories six transistor 6T based static random access memory SRAM continues to play a pivotal role in nearly all VLSI systems due to its superior speed and full compatibility with logic process technology But as the technology scaling continues SRAM design is facing severe challenge in mainta ing suf cient cell stability margin under relentless area scaling Meanwhile rapid expansion in mobile application including new emerging application in sensor and medical devices requires far more aggressive voltage scaling to meet very str gent power constraint Many innovative circuit topologies and techniques have been extensively explored in recent years to address these challenges **Ultra Wideband** Ranjit Gharpurey,Peter Kinget,2008-03-06 Recent advances in wireless communication technologies have had a transfor tive impact on society and have directly contributed to several economic and social aspects of daily life Increasingly the untethered exchange of information between devices is becoming a prime requirement for further progress which is placing an ever greater demand on wireless bandwidth The ultra wideband UWB system marks a major milestone in this progress Since 2002 when the FCC allowed the unlicensed use of low power UWB radio signals in the 3 1 10 6GHz frequency band there has been signi cant synergistic advance in this technology at the c cuits architectural and communication systems levels This technology allows for devices to communicate wirelessly while coexisting with other users by ensuring that its power density is suf ciently low so that it is perceived as noise to other users UWB is expected to address existing needs for high data rate short range c munication applications between devices such as computers and peripherals or consumer electronic devices In the long term it makes available spectrum to periment with new signaling formats such as those based on very short pulses of radio frequency RF energy As such it represents an opportunity to design fun mentally different wireless systems which rely on the bandwidth of the signals to enhance the data rate or which use the available bandwidth for diverse applications such as ranging and biomedical instrumentation

Carbon Nanotube Electronics Ali Javey,Jing Kong,2009-04-21 This book provides a complete overview of the field of carbon nanotube electronics It covers materials and physical properties synthesis and fabrication processes devices and circuits modeling and finally novel applications of nanotube based electronics The book introduces fundamental device physics and circuit concepts of 1 D electronics At the same time it provides specific examples of the state of the art nanotube devices *Electronic Design Automation for IC System Design, Verification, and Testing* Luciano Lavagno,Igor L. Markov,Grant Martin,Louis K. Scheffer,2017-12-19 The first of two volumes in the Electronic Design Automation for Integrated Circuits Handbook Second Edition Electronic Design Automation for IC System Design Verification and Testing thoroughly examines system level design microarchitectural design logic verification and testing Chapters contributed by leading experts authoritatively discuss processor modeling and design tools using performance metrics to select microprocessor cores for integrated circuit IC designs design and verification languages digital simulation hardware

acceleration and emulation and much more New to This Edition Major updates appearing in the initial phases of the design flow where the level of abstraction keeps rising to support more functionality with lower non recurring engineering NRE costs Significant revisions reflected in the final phases of the design flow where the complexity due to smaller and smaller geometries is compounded by the slow progress of shorter wavelength lithography New coverage of cutting edge applications and approaches realized in the decade since publication of the previous edition these are illustrated by new chapters on high level synthesis system on chip SoC block based design and back annotating system level models Offering improved depth and modernity Electronic Design Automation for IC System Design Verification and Testing provides a valuable state of the art reference for electronic design automation EDA students researchers and professionals

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