

Data Access and Storage Management for Embedded Programmable Processors

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Data Access and Storage Management for Embedded Programmable Processors Francky Catthoor, K. Danckaert, 2002-03-31 ndice abreviado 1 DTSE in programmable architectures 2 Related compiler work on data transfer and storage management 3 Global loop transformations 4 System level storage requirement estimation 5 Automated data reuse exploration techniques 6 Storage cycle budget distribution 7 Cache optimization 8 Demonstrator designs 9 Conclusions and future work

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Computer Information Systems and Industrial Management Khalid Saeed, Władysław Homenda, 2018-09-17 This book constitutes the proceedings of the 17th International Conference on Computer Information Systems and Industrial Management Applications CISIM 2018 held in Olomouc Czech Republic in September 2018 The 42 full papers presented together with 4 keynotes were carefully reviewed and selected from 69 submissions The main topics covered by the chapters in this book are biometrics security systems multimedia classification and clustering and industrial management Besides

these the reader will find interesting papers on computer information systems as applied to wireless networks computer graphics and intelligent systems The papers are organized in the following topical sections biometrics and pattern recognition applications computer information systems industrial management and other applications machine learning and high performance computing modelling and optimization and various aspects of computer security

Energy-Aware Memory Management for Embedded Multimedia Systems Florin Balasa,Dhiraj K. Pradhan,2011-11-16 Energy Aware Memory Management for Embedded Multimedia Systems A Computer Aided Design Approach presents recent computer aided design CAD ideas that address memory management tasks particularly the optimization of energy consumption in the memory subsystem It explains how to efficiently implement CAD solutions including theoretical methods an

Low-Power Processors and Systems on Chips Christian Piguet,2018-10-03 The power consumption of microprocessors is one of the most important challenges of high performance chips and portable devices In chapters drawn from Piguet s recently published Low Power Electronics Design this volume addresses the design of low power microprocessors in deep submicron technologies It provides a focused reference for specialists involved in systems on chips from low power microprocessors to DSP cores reconfigurable processors memories ad hoc networks and embedded software Low Power Processors and Systems on Chips is organized into three broad sections for convenient access The first section examines the design of digital signal processors for embedded applications and techniques for reducing dynamic and static power at the electrical and system levels The second part describes several aspects of low power systems on chips including hardware and embedded software aspects efficient data storage networks on chips and applications such as routing strategies in wireless RF sensing and actuating devices The final section discusses embedded software issues including details on compilers retargetable compilers and coverification tools Providing detailed examinations contributed by leading experts Low Power Processors and Systems on Chips supplies authoritative information on how to maintain high performance while lowering power consumption in modern processors and SoCs It is a must read for anyone designing modern computers or embedded systems

Dynamic Memory Management for Embedded Systems David Atienza Alonso,Stylios Mamagkakis,Christophe Poucet,Miguel Peón-Quirós,Alexandros Bartzas,Francky Catthoor,Dimitrios Soudris,2014-09-19 This book provides a systematic and unified methodology including basic principles and reusable processes for dynamic memory management DMM in embedded systems The authors describe in detail how to design and optimize the use of dynamic memory in modern multimedia and network applications targeting the latest generation of portable embedded systems such as smartphones Coverage includes a variety of design and optimization topics in electronic design automation of DMM from high level software optimization to microarchitecture level hardware support The authors describe the design of multi layer dynamic data structures for the final memory hierarchy layers of the target portable embedded systems and how to create a low fragmentation cost efficient dynamic memory management subsystem out of configurable components for the particular memory allocation and de

allocation patterns for each type of application The design methodology described in this book is based on propagating constraints among design decisions from multiple abstraction levels both hardware and software and customizing DMM according to application specific data access and storage behaviors Ultra-Low Energy Domain-Specific Instruction-Set Processors Francky Catthoor, Praveen Raghavan, Andy Lambrechts, Murali Jayapala, Angeliki Kritikakou, Javed Absar, 2010-08-05 Modern consumers carry many electronic devices like a mobile phone digital camera GPS PDA and an MP3 player The functionality of each of these devices has gone through an important evolution over recent years with a steep increase in both the number of features as in the quality of the services that they provide However providing the required compute power to support an uncompromised combination of all this functionality is highly non trivial Designing processors that meet the demanding requirements of future mobile devices requires the optimization of the embedded system in general and of the embedded processors in particular as they should strike the correct balance between flexibility energy efficiency and performance In general a designer will try to minimize the energy consumption as far as needed for a given performance with a sufficient flexibility However achieving this goal is already complex when looking at the processor in isolation but in reality the processor is a single component in a more complex system In order to design such complex system successfully critical decisions during the design of each individual component should take into account effect on the other parts with a clear goal to move to a global Pareto optimum in the complete multi dimensional exploration space In the complex global design of battery operated embedded systems the focus of Ultra Low Energy Domain Specific Instruction Set Processors is on the energy aware architecture exploration of domain specific instruction set processors and the co optimization of the datapath architecture foreground memory and instruction memory organisation with a link to the required mapping techniques or compiler steps at the early stages of the design By performing an extensive energy breakdown experiment for a complete embedded platform both energy and performance bottlenecks have been identified together with the important relations between the different components Based on this knowledge architecture extensions are proposed for all the bottlenecks **Energy-Efficient Communication Processors** Robert Fasthuber, Francky Catthoor, Praveen Raghavan, Frederik Naessens, 2013-05-29 This book describes a new design approach for energy efficient Domain Specific Instruction set Processor DSIP architectures for the wireless baseband domain The innovative techniques presented enable co design of algorithms architectures and technology for efficient implementation of the most advanced technologies To demonstrate the feasibility of the author s design approach case studies are included for crucial functionality of advanced wireless systems with increased computational performance flexibility and reusability Designers using this approach will benefit from reduced development product costs and greater scalability to future process technology nodes *Source Code Optimization Techniques for Data Flow Dominated Embedded Software* Heiko Falk, Peter Marwedel, 2013-03-19 This book focuses on source to source code transformations that remove addressing related overhead present in most multimedia or

signal processing application programs This approach is complementary to existing compiler technology What is particularly attractive about the transformation flow presented here is that its behavior is nearly independent of the target processor platform and the underlying compiler Hence the different source code transformations developed here lead to impressive performance improvements on most existing processor architecture styles ranging from RISCs like ARM7 or MIPS over Superscalars like Intel Pentium PowerPC DEC Alpha Sun and HP to VLIW DSPs like TI C6x and Philips TriMedia The source code did not have to be modified between processors to obtain these results Apart from the performance improvements the estimated energy is also significantly reduced for a given application run These results were not obtained for academic codes but for realistic and representative applications all selected from the multimedia domain That shows the industrial relevance and importance of this research At the same time the scientific novelty and quality of the contributions have led to several excellent papers that have been published in internationally renowned conferences like e g DATE This book is hence of interest for academic researchers both because of the overall description of the methodology and related work context and for the detailed descriptions of the compilation techniques and algorithms

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