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Algorithm Designs , *ICT Innovations 2012* Smile Markovski, Marjan Gusev, 2013-03-26 The present stage of the human civilization is the e society which is build over the achievements obtained by the development of the information and communication technologies It affects everyone from ordinary mobile phone users to designers of high quality industrial products and every human activity from taking medical care to improving the state governing The science community working in computer sciences and informatics is therefore under constant challenge it has to solve the new appeared theoretical problem as well as to find new practical solutions The fourth ICT Innovations Conference held in September 2012 in Ohrid Macedonia was one of the several world wide forums where academics professionals and practitioners presented their last scientific results and development applications in the fields of high performance and parallel computing bioinformatics human computer interaction security and cryptography computer and mobile networks neural networks cloud computing process verification improving medical care improving quality of services web technologies hardware implementations cultural implication In this book the best 37 ranked articles are presented **Microprocessor-Based Parallel Architecture for Reliable Digital Signal Processing Systems** Alan D. George, 2018-01-18 This book presents a distributed multiprocessor architecture that is faster more versatile and more reliable than traditional single processor architectures It also describes a simulation technique that provides a highly accurate means for building a prototype system in software The system prototype is studied and analyzed using such DSP applications as digital filtering and fast Fourier transforms The code is included as well which allows others to build software prototypes for their own research systems The design presented in Microprocessor Based Parallel Architecture for Reliable Digital Signal Processing Systems introduces the concept of a dual mode architecture that allows users a dynamic choice between either a conventional or fault tolerant system as application requirements dictate This volume is a must have for all professionals in digital signal processing parallel and distributed computer architecture and fault tolerant computing

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