

Chapter 3 SOLUTIONS

1) a) $I_D = \frac{V_{in} - 2V_{D0n}}{R_1 + R_2}$
 $= \frac{2.5 - 1.4}{4k\Omega} = \frac{1.1V}{4k\Omega}$

$I_D = 275\mu A$

b) $I_S = 10^{-14} A$, $T = 300K$, $V_{D0n} = 0.7V$

$I_D = I_S (e^{V_D/\phi_T} - 1)$

where $\phi_T = \frac{kT}{q} = 26mV @ 300K$

★ $I_D = \frac{V_{in} - 2V_D}{R_1 + R_2} = I_S (e^{V_D/\phi_T} - 1)$
 $\frac{2.5 - 2V_D}{4k\Omega} = 10^{-14} (e^{V_D/0.026} - 1)$

iterating on this expression
 we can obtain.

$V_D = 0.628V$
 $I_D = 311\mu A$

c) SPICE

exercise 3.1

vin in 0 dc 2.5
 r1 in 2 2k
 d1 2 3 D
 r2 3 out 2k
 d2 out 0 D

.options post=2
 .op

.model D D level=1 is=1e-14 nr=0.5
 .end

 NODE VOLTAGE
 2 1.8750E+00
 3 1.2500E+00
 OUT 6.2501E-01

CURRENT 3.1249E-04

d) $I_S = 10^{-14} A$, $T = 300K$;
 again use ★ from b and iterate
 to find:

$V_D = 0.743V$
 $I_D = 253\mu A$

$I_S = 10^{-14} A$, $T = 350K$;

$V_D = 0.728V$
 $I_D = 261\mu A$

exercise 3.1d
 vin in 0 dc 2.5
 r1 in 2 2k
 d1 2 3 D
 r2 3 out 2k
 d2 out 0 D

.options post=2
 .op

* temp 27
 .temp 77
 * model D D level=1 is=1e-14 nr=0.5
 .model D D level=1 is=1e-14 nr=0.5
 .end

 t=27C
 NODE VOLTAGE
 2 1.9889E+00
 3 1.2500E+00
 IN 2.5000E+00
 OUT 7.3892E-01

ID 2.5554E-04

 t=77C
 NODE VOLTAGE
 2 1.7844E+00
 3 1.2500E+00
 IN 2.5000E+00
 OUT 5.3436E-01
 ID 3.3782E-04

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Lingjun Ying



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Integrated Circuit and System Design. Power and Timing Modeling, Optimization and Simulation Vassilis Paliouras, 2005-09-06 This book constitutes the refereed proceedings of the 15th International Workshop on Power and Timing Optimization and Simulation PATMOS 2005 held in Leuven Belgium in September 2005 The 74 revised full papers presented were carefully reviewed and selected from numerous submissions The papers are organized in topical sections on low power processors code optimization for low power high level design telecommunications and signal processing low power circuits system on chip design busses and interconnections modeling design automation low power techniques memory and register files applications digital circuits and analog and physical design

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Forthcoming Books Rose Arny, 2002-04 *The Proceedings of the European Design Automation Conference*, 1990

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