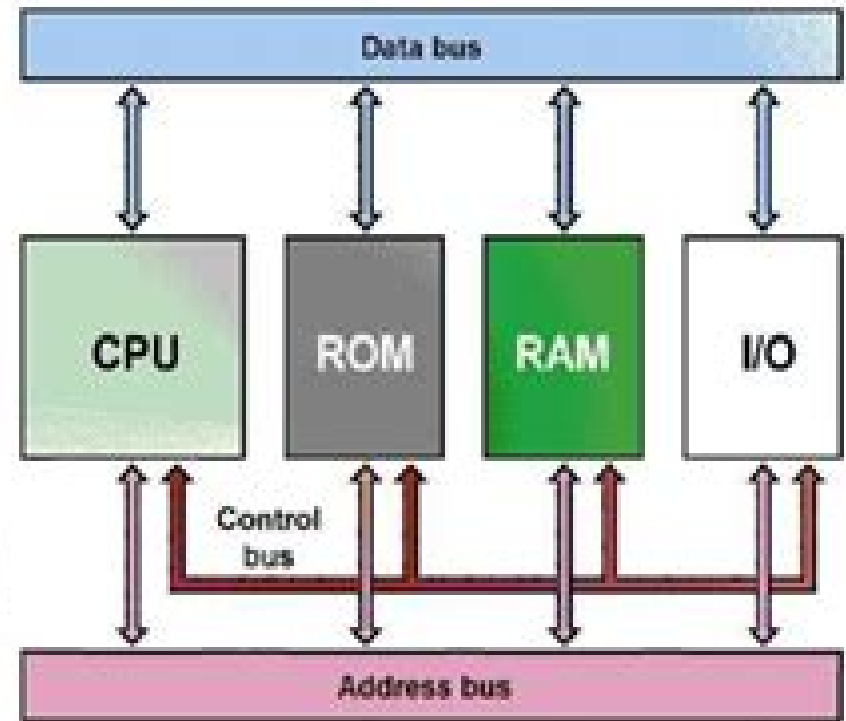


Architecture and components of computer systems.



Computer Architecture Computer Architecture

P N Basu



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High-performance Computer Architecture Harold S. Stone, 1993 This update of the popular book on computer architecture presents design ideas embodied in many high performance machines and stresses techniques for evaluating them Stone develops a proper understanding of the design process by treating the various trade offs that exist in designing choices and shows how good designs make efficient use of technology Features Teaches techniques for the design and analysis of high performance machines Develops students intuition for design by treating various tradeoffs that exist in design choices Discusses many important topics RISC architectures interconnection meshes Cache coherent and multiprocessors and Cache Memory Includes enhanced descriptions of RISC Processors Expands material on Cache Memory Analysis Current technology in RISC with a focused look on super scalar Additional memory models and techniques for doing Cache design New proposals for coherent memory systems in System C parallel processors Both design and thought problems and problems with limiting parameters are provided 0201526883B04062001

Computer Architecture and Organization Miles J. Murdocca, Vincent P. Heuring, 2007-03-16 An accessible introduction to computer systems and architecture Anyone aspiring to more advanced studies in computer related fields must gain an understanding of the two parallel aspects of the modern digital computer programming methodology and the underlying machine architecture The uniquely integrated approach of Computer Architecture and Organization connects the programmer's view of a computer

system with the associated hardware and peripheral devices providing a thorough three dimensional view of what goes on inside the machine Covering all the major topics normally found in a first course in computer architecture the text focuses on the essentials including the instruction set architecture ISA network related issues and programming methodology Using real world case studies to put the information into perspective the chapters examine Data representation Arithmetic The instruction set architecture Datapath and Control Languages and the machine Memory Buses and peripherals Networking and communication Advanced computer architecture A valuable feature of this book is the use of ARC a subset of the SPARC processor for an instruction set architecture A platform independent ARCTools suite containing an assembler and simulator for the ARC ISA that supports the examples used in the book is available Better yet the content is supplemented by online problem sets available through WileyPlus Balanced and thoughtfully designed for use as either a classroom text or self study guide Computer Architecture and Organization An Integrated Approach will put you solidly on track for advancing to higher levels in computer related disciplines About the Author MILES MURDOCCA serves as the President and CEO of Internet Institute USA IIUSA a private postsecondary information technology IT school specializing in networking operating systems IP telephony programming and security Previously Dr Murdocca has been a computer science faculty member at Rutgers University and a research scientist at AT T Bell Laboratories working in computer architecture networking and digital optical computing He is the author of A Digital Design Methodology for Optical Computing and Principles of Computer Architecture and a contributing author to Computer Systems Design and Architecture Second Edition as well as the author of dozens of professional papers and patents relating to information technology VINCE HEURING is an associate professor and acting chair of the Department of Electrical and Computer Engineering at the University of Colorado at Boulder He has been at the university since 1984 and prior to that he spent three years at the University of Cincinnati Professor Heuring's research encompasses computer architectures and programming language design implementation He and his colleague Harry Jordan designed and built the world's first stored program optical computer SPOC *Advances in Computer Architecture* Glenford J. Myers, 1978 [Computer Architecture Technology Trends](#) Architecture Technology Corpor, 2013-10-22 Please note this is a Short Discount publication This year's edition of Computer Architecture Technology Trends analyses the trends which are taking place in the architecture of computing systems today Due to the sheer number of different applications to which computers are being applied there seems no end to the different adoptions which proliferate There are however some underlying trends which appear Decision makers should be aware of these trends when specifying architectures particularly for future applications This report is fully revised and updated and provides insight into the fundamentals of computer architecture what it is and how it is applied to fit a particular problem definition Also discussed is where the future leads given current trends in computer architecture **Computer Organization And Architecture** P N Basu, The book covers the syllabi of Computer Organization and Architecture for most of the Indian universities and colleges The author has

carefully arranged the chapters and topics using Education Technology and Courseware Engineering Principles with proper planning to help self paced as well as guided learning Large numbers of examples solved problems and exercises have been incorporated to help students strengthen their base in the subject A number of multiple choice questions have been included with answers and explanatory notes The basic principles have been explained with appropriate lucid descriptions supported by explanatory diagrams and graphics The advanced principles have been presented with in depth explanation and relevant examples

Introduction to Computer Architecture and Organization Harold Lorin, 1989-05-03 An introduction to the nature of computer architecture and organization Presents interesting problems with elegant solutions with emphasis on the abstract elements of the problems common to all computer design Addresses the several schools of thought on what constitutes a good computer architecture focusing on the current RISC versus non RISC approaches Also discusses the downward drift of design sophistication to smaller machines such as pipelines caches and overlapped I O Includes many examples of specific machines and the design philosophy behind them

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